

## **EVALUATION OF MODERN GPGPU TECHNOLOGIES FOR IMAGE PROCESSING**

JOACHIM MEYER IMAGE PROCESSING GPGPU ENGINEER

**VISION. RIGHT.** NOW.



## (TOO?) MANY DIFFERENT GPGPU PROGRAMMING MODELS / APIS







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#### read\_accessor inAcc\_; write\_accessor outAcc\_;

SizeXY size\_;
SizeXY outSize\_;

PlaneExtractWorker(SizeXY size, SizeXY outSize, read\_accessor in, write\_accessor out)
 : inAcc\_(in), outAcc\_(out), size\_(size), outSize\_(outSize){}

void operator()(sycl::nd\_item<2> itm)

size\_t x = itm.get\_global\_id(0), y = itm.get\_global\_id(1);

if(x >= (outSize\_[0] / 2) || y >= (outSize\_[1] / 2))
| return;

size\_t xIn = (itm.get\_global\_id(0) + 1) \* 2, yIn = (itm.get\_global\_id(1) + 1) \* 2;

size\_t row = xIn + (yIn - 1) \* size\_[0]; typename dt::tmp\_vec4 line0{inAcc\_[row - 1], inAcc\_[row], inAcc\_[row + 1], inAcc\_[row + 2]};

row = xIn + (yIn)\*size\_[0]; typename dt::tmp\_vec4 line1{inAcc\_[row - 1], inAcc\_[row], inAcc\_[row + 1], inAcc\_[row + 2]};



#### AGENDA

- SELECTION OF COMPARED APIS
- EVALUATION SETUP
- PERFORMANCE
- USABILITY
- PLATFORM INDEPENDENCE
- CONCLUSION
- FUTURE PROSPECTS





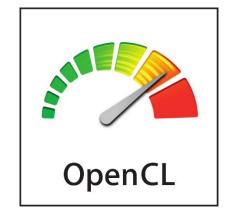


#### BASICS

#### Selection of APIs













#### BASICS

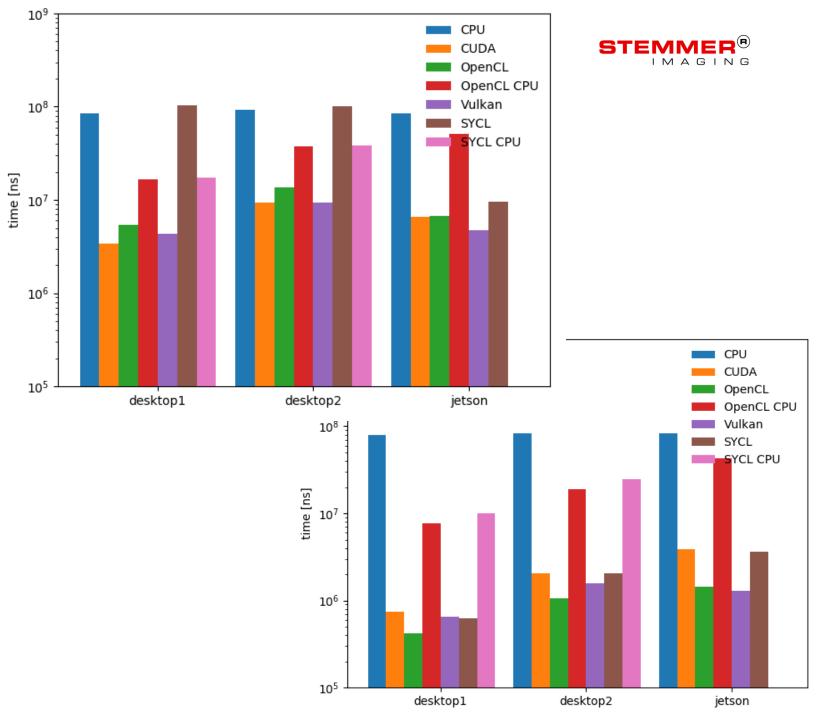
Test Project setup

- Targeting all 4 APIs + CPU reference implementation
- Targeted devices: CPUs & GPUs
- OSs: Windows & Linux 64bit
- Implementations:
  - CUDA 10.1
  - Vulkan 1.1
  - OpenCL 1.2
  - ComputeCpp (Win) & hipSYCL (Linux)
- Algorithms for polarization camera image processing



It's comparable.

#### PERFORMANCE



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How hard is it?

#### USABILITY

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std::fstream ifs[sourceDir.string(), std::los::binary | std::los::in | std::los::ate);
if(lifs.is\_open())
{

size\_t size = ifs.tellg();
ifs.seekg(0, std::ios::beg);

std::uint32\_t filesizepadded = static\_castestd::uint32\_t>(ceil(size / 4.0)) \* 4;

// read file contents. char \*str = new char[filesizepadded]; ifs.read(str, size); ifs.close();

for(size\_t i = size; i < filesizepadded; i++)</pre>

)

length = filesizepadded; return (uint32\_t \*)str;

Shader(const Shader &) = delete; void operator=(const Shader &) = delete;

Shader(Shader &&rhs) : module\_(std::move(rhs.module\_)), ir\_(std::move(rhs.ir\_)) {

Shader Soperator=(Shader SSrhs)

module\_ = std::move(rhs.module\_); ir\_ = std::move(rhs.ir\_); return \*this;

vk::ShaderModule &ShaderModule()

return \*module\_;

static Shader FromGisiFile(std::string path, std::vectorcstd::paircstd::string, std::string>> macros, SharedVulkanProvider provider)

shaderc::Compiler compiler; shaderc::CompileOptions opts; for(auto macro : macros)

opts.AddMacroDefinition(macro.first, macro.second);

opts.SetTargetEnvironment(shaderc\_target\_env\_wulkan, shaderc\_env\_worsion\_wulkan\_1\_i); opts.SetTortimizationtewul(shaderc\_optimization\_level:shaderc\_eptimization\_level\_performance); opts.SetTortumer(std:smake\_umiquecShaderincLuders()); auto source = Sourcefile::FromFile(path);

auto result = compiler.CompileGisIToSpv(source(), shaderc\_shader\_kind::shaderc\_compute\_shader, path.c\_str(), "main", opts);
if(result.GetCompilationStatus() != shaderc\_compilation\_status\_success)

[ throw compilation\_error{std::string("Compiling ") + path + std::string(" failed with:\n") + result.GetErrorMessage());
}

std::vectorcstd::uint32\_t> binary(result.cbegin(), result.cend());

vk::ShaderWoduleCreateInfo createInfo{{}, binary.size() \* 4, binary.data()};

auto module = provider->CreateShaderHodule(createInfo); auto parser = spiry\_cross::Parser[std::move(binary)); parser.parse();

return {std::move(module), std::move(parser.get\_parsed\_ir())};

static Shader FromFile(std::string path, SharedVulkanProvider provider)

std::uint32\_t size; std::uint32\_t \*shaderData = readFile(size, path); vk::ShaderModuleCreateInFo createInFo({}, size, shaderData};

auto module = provider->CreateShaderHodule(createInfo); spiry\_cross::Compiler\_comp[shaderData, size]; delute[] shaderData;

auto parser = spirv\_cross::Parser(shader0ata, size);
parser.parse();

return {std::move(module), std::move(parser\_get\_parsed\_ir())};

} // namespace Vulkan
} // namespace GPGPUEvaluatio



Timeline View 👻	
	1,61s 5s 10s
▼	
<ul> <li>Threads (17)</li> </ul>	
▼ ✔ [8968] -	
CUDA API	
Vulkan API	
Profiler overhead	
✓ [9312] -	
15 threads hidden 🛛 💻 🛨	
<ul> <li>CUDA (GeForce GTX 770, 0000:</li> </ul>	
<ul> <li>Frame duration (60 FPS)</li> </ul>	
<ul> <li>CPU frame duration</li> </ul>	
<ul> <li>GPU frame duration</li> </ul>	
▼ Vulkan	
<ul> <li>Command Buffers Creation</li> </ul>	
▼ GPU	
<ul> <li>Queue 0 (Gfx/Comp/Xfer)</li> </ul>	
API	





## WHAT'S THE IMPLEMENTATION COST?

	CUDA	SYCL	OpenCL	Vulkan
LoC basic setup	4	5	6	65
LoC realistic setup	25	27	34	128 (+ 25 GLSL→SPIRV)
LoC / new kernel	4	5	6	11
C++ kernels	✓	✓	✓	
Implicit asynchronity	~	~	✓	
Taskgraph	✓	~		

#### TOOLS MAKE DEVELOPMENT EASIER



## **ANY TOOLS TO HELP?**

CUDA	SYCL	OpenCL	Vulkan
<ul> <li>Solid dev tooling:</li> <li>kernel debugging</li> <li>profiling</li> <li>IDE integration</li> </ul>	<ul> <li>Hardly any specific tools, but native OpenCL / HIP tools usable</li> <li>Host-device enables native IDE debugging</li> </ul>	<ul> <li>Mostly vendor specific dev tools</li> <li>LPGPU<sup>2</sup> CodeXL: generalization of AMD project</li> </ul>	<ul> <li>Mainly graphics focused tooling</li> <li>Validation layers</li> <li>Emulator (Talvos)</li> </ul>



#### LIBRARIES REDUCE DEVELOPMENT COST



## LIBRARIES?

CUDA	SYCL	OpenCL	Vulkan
<ul> <li>Many optimized libraries</li> <li>FFT, BLAS, image processing,</li> </ul>	<ul> <li>Some libraries</li> <li>BLAS, DNN, RNG, Parallel STL, image processing</li> <li>Native (OpenCL / HIP) libraries usable</li> </ul>	<ul> <li>Number of libraries with some device- specific optimization</li> <li>FFT, BLAS, DGEMM, image processing,</li> </ul>	Hardly any Compute specific libraries



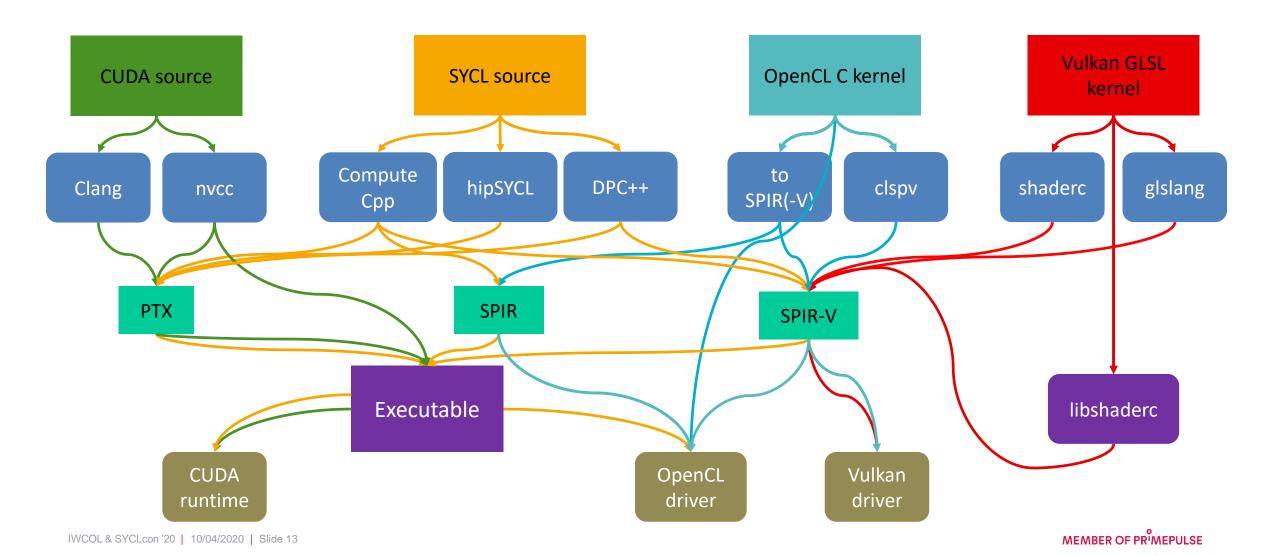


# **HELP ANYONE?**

CUDA	SYCL	OpenCL	Vulkan
<ul> <li>Widely used by scientists and application devs</li> <li>De-facto standard in ML libraries</li> </ul>	<ul> <li>Few applications known</li> <li>Tensorflow</li> <li>Eigen</li> </ul>	<ul> <li>Wide adoption in consumer applications</li> <li>Adobe Creative Cloud</li> <li>Final Cut Pro</li> </ul>	<ul> <li>Increasing adoption for mobile device support / combined with graphics</li> <li>Adobe Premier Rush</li> <li>OcataneRender</li> </ul>
• SO Questions: 12.380	• SO Questions: 28	• SO Questions: 5.040	• SO Questions: 1.020

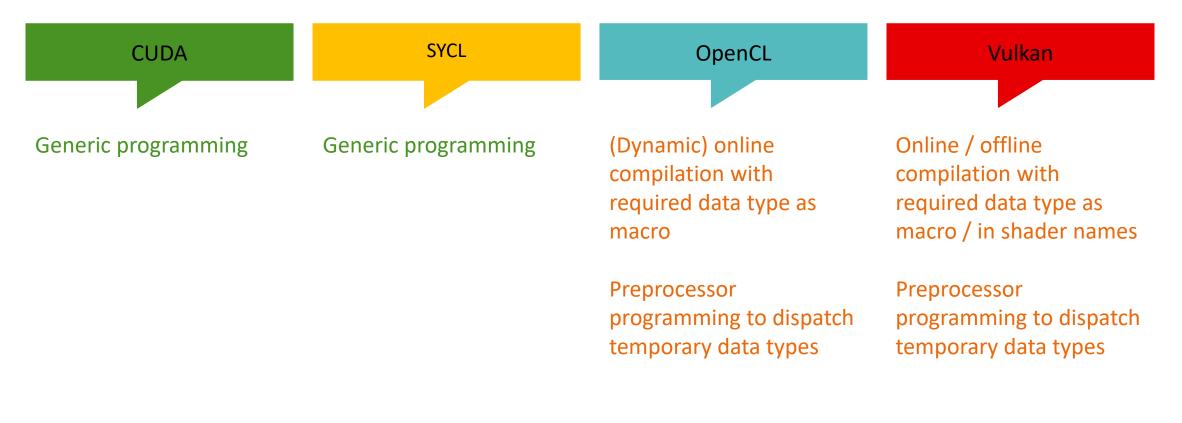


## **HOW DOES THE CODE COME TO LIFE?**





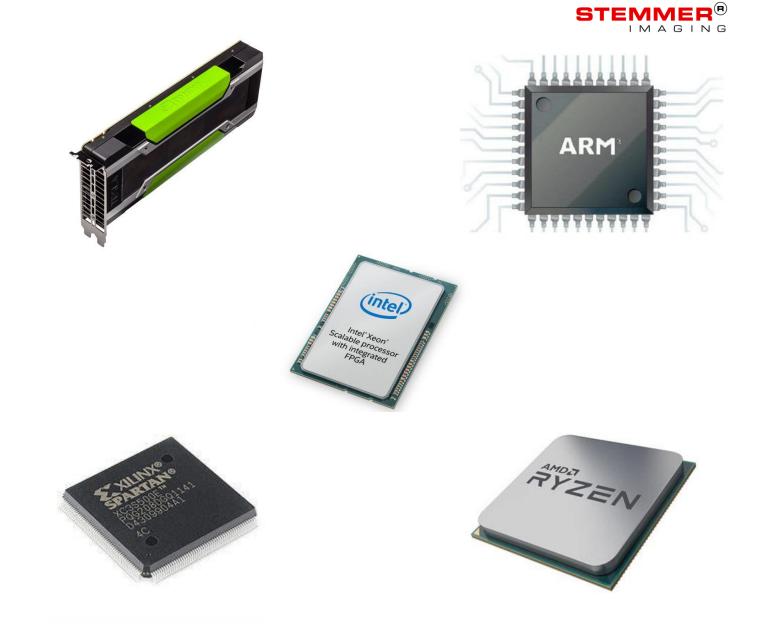
## HOW TO HANDLE DYNAMIC DATA TYPES?







# PLATFORM INDEPENDENCE







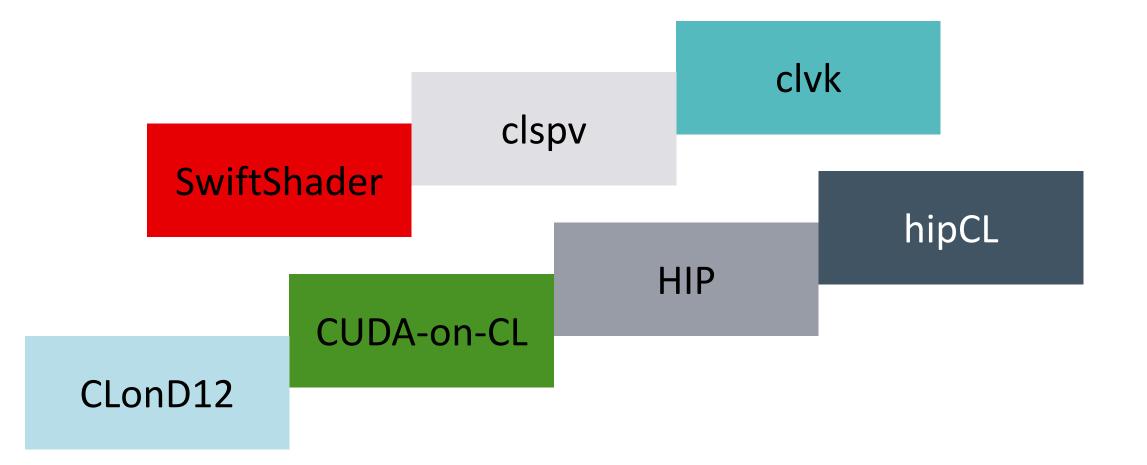
#### **CAN IT TARGET XYZ?**

	CUDA	SYCL	OpenCL	Vulkan
Most recent version	10.2	1.2.1	2.2	1.2
Nvidia	10.2	1.2.1	1.2	1.2
AMD	HIP	1.2.1	2.0	1.2
Intel		1.2.1	2.1	1.2
ARM		1.2.1	2.1	1.2
Windows	✓	✓	✓	✓
Linux	✓	✓	✓	✓
macOS	<		<	✓
Android			<	✓
CPU		✓	✓	✓
FPGAs		✓	✓	

PROJECTS INCREASING PORTABILITY OF THE APIS



#### **PORTABILITY INITIATIVES**







# CONCLUSION

So which API should be used?

CUDA	SYCL	OpenCL	Vulkan
Single-source programming	Single-source programming	Cross-platform (incl. FPGAs,)	Fully OS and GPU-vendor independent
Highly optimized and powerful	Multi-platform (incl. FPGAs,)	Mature libraries	High setup cost
libraries and tools	Tools for	Big community	but possibility to optimize
Vendor lock-in acceptable? (Maybe use HIP	underlying implementation usable	Not-up-to-date implementations	Lack of compute specific tooling & libraries
instead?)	Emerging SYCL- specific tool and library support		

# FUTURE PROSPECTS



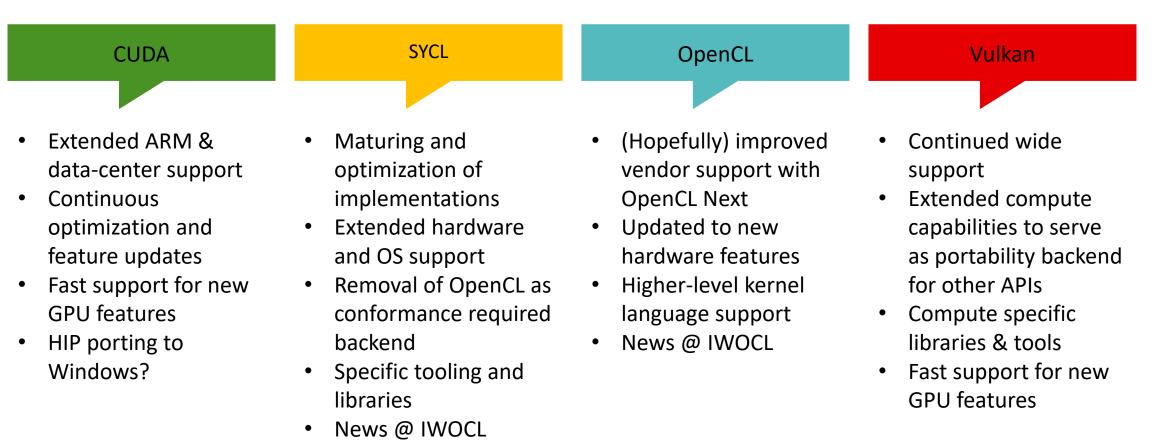
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#### SOME POSSIBLE DEVELOPMENTS

# WHAT'S UP NEXT?



SYCL-on-Vulkan?



# THANK YOU VERY MUCH FOR YOUR ATTENTION

#### **JOACHIM MEYER**

STEMMER IMAGING AG J.MEYER@STEMMER-IMAGING.COM STEMMER-IMAGING.COM JOAMEYER.DE

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