The 11th International workshop on OpenCL and SYCL

IWOCL & SYCLcon 2023

Towards Alignment of Parallelism in SYCL and ISO C++

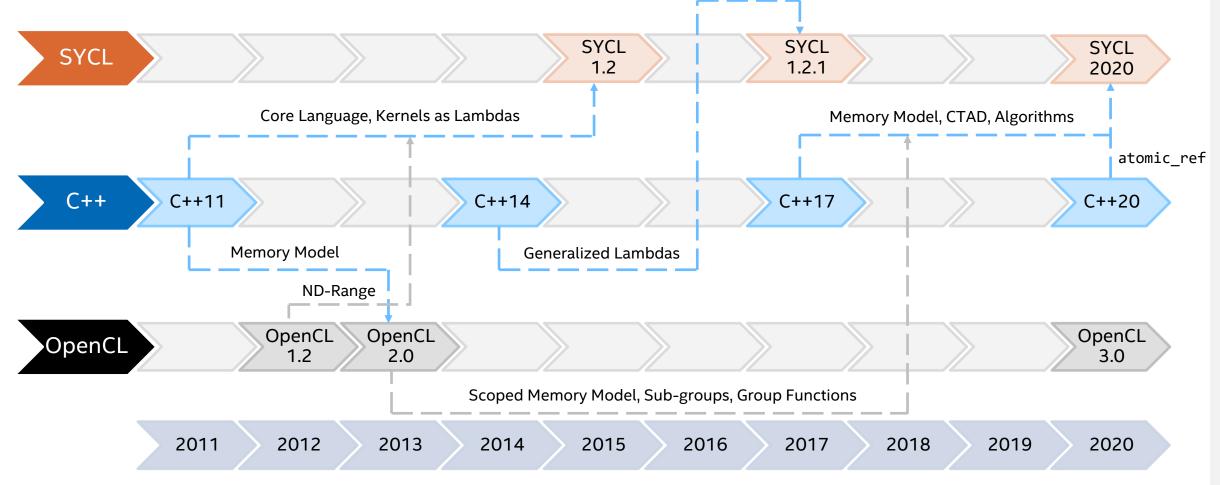
John Pennycook, Intel Corporation

Ben Ashbaugh, James Brodman, Michael Kinsner, Steffen Larsen, Greg Lueck, Roland Schulz, Michael Voss

April 18-20, 2023 | University of Cambridge, UK



Parallel Evolution of SYCL, ISO C++ and OpenCL



Maintaining alignment between these specifications requires constant, ongoing effort!

Motivating Use-Case: Global Synchronization

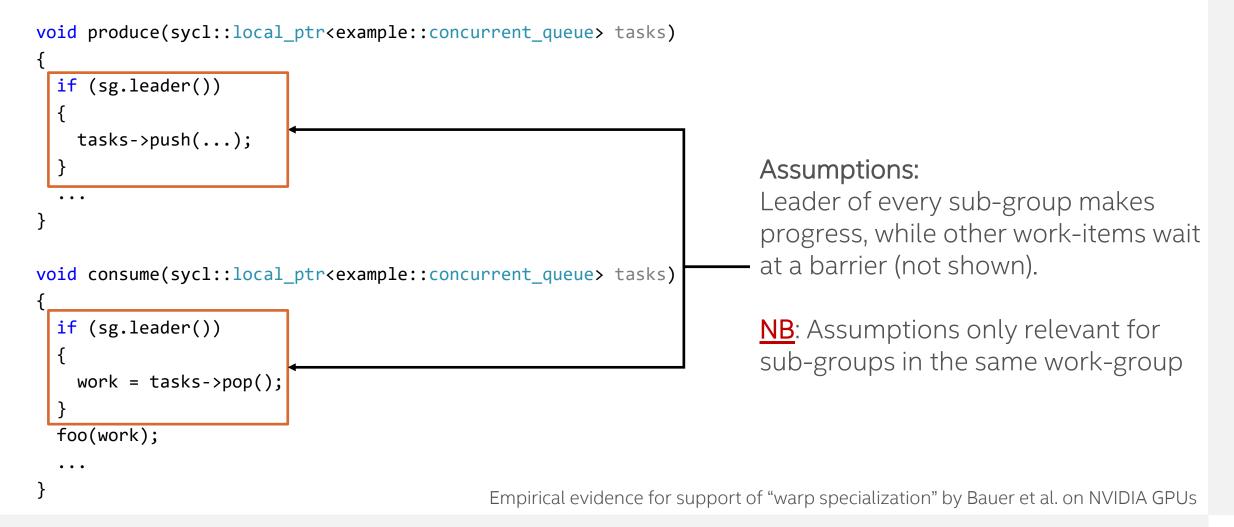
Should this code work? Does it?

```
template <size t Dimensions>
void arrive_and_wait(size_t expected, sycl::group<Dimensions> wg, ...)
 // Wait for all work-items in the group before signaling arrival
 sycl::group barrier(wg);
                                                            Assumption 1:
 // Elect one work-item to synchronize with other groups
                                                            Leader of the work-group makes progress
 if (wg.leader()) {
                                                            while other work-items wait at second barrier.
   // Signal that this group has arrived at the barrier
   atomic_counter++;
                                                       Assumption 2:
   // Spin while waiting for all groups to arrive
                                                       Every work-group leader makes progress.
   while (atomic counter.load() != expected) {}
 // Wait for the leader to finish synchronizing with other groups
 sycl::group barrier(wg);
```

Empirical evidence for support under "occupancy-bound execution" by Sorensen et al. on multiple GPUs

Motivating Use-Case: Sub-group Specialization

Should this code work? Does it?



4

Forward Progress Guarantees in ISO C++

Guarantee	Concurrent	Parallel	Weakly Parallel
Eventually executes its first step	\checkmark	×	×
Makes progress after executing its first step	\checkmark	\checkmark	×

Mental Model	Mental Model	Mental Model
OS threads	Tasks	≈Fibers?
Provided By	Provided By	Provided By
std::thread	par	par_unseq

Forward Progress Guarantees in SYCL?

Guarantee	Work-item in parallel_for	Work-item in ND-Range parallel_for
Eventually executes its first step	×	×
Makes progress after executing its first step	×	×
Makes progress when other work-items hit a barrier	N/A	?

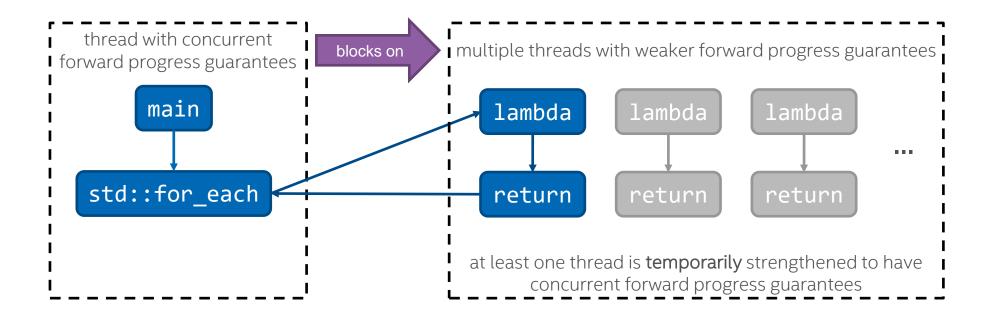
SYCL 2020, Revision 6, Section 3.8.3.4:

"A SYCL implementation must execute work-items concurrently[†] and must ensure that the work-items in a group **obey the semantics of group barriers**, but are **not required to provide any additional forward progress guarantees**"

[†] Not "concurrent forward progress guarantees"!

"Blocking with Forward Progress Guarantee Delegation" in ISO C++

// Assume calling thread has concurrent forward progress guarantees
std::for_each(std::par_unseq, c.begin(), c.end(), [&](auto x)
{
 ... // Each invocation has weakly parallel forward progress guarantees
}); // Calling thread blocks with forward progress delegation



Forward Progress Guarantees in SYCL (Revisited)

Guarantee	Work-item in parallel_for	Work-item in ND-Range parallel_for
Eventually executes its first step	×	×
Makes progress after executing its first step	×	×
Makes progress when other work-items hit a barrier	N/A	

Proposed Fixes to Section 3.8.3.4:

- 1. "Each work-item ... is a separate thread of execution, providing at least weakly parallel forward progress guarantees."
- 2. "When a work-item arrives at a group barrier acting on group G, **implementations must eventually select and potentially strengthen another work-item in group G** that has not yet arrived at the barrier."

Hypothetical: SYCL Implemented with ISO C++

```
template <typename Kernel>
void handler::parallel for(sycl::nd range<1> ndr, Kernel f) {
 std::vector<size_t> groups = { 1, 2, ..., ndr.get_group_range()[0] };
 std::vector<size t> items = { 1, 2, ..., ndr.get local range()[0] };
 // Create a thread of execution providing parallel forward progress guarantees per work-group
 std::for each(std::execution::par, std::begin(groups), std::end(groups), [&](size t group id) {
   // Create a thread of execution providing weakly parallel forward progress guarantees per work-item
    std::for each(std::execution::par unseq, std::begin(items), std::end(items), [&](size t item id) {
     // Invoke the user supplied kernel function object
     sycl::nd item<1> item = sycl::detail::make nd item<1>(group id, item id);
     f(item);
                                                                                  NB: Not all threads of execution
   });
                                                                                  are created at the same time.
 });
```

Pseudocode of a hypothetical implementation for illustrative purposes only.

New Mental Model: A Hierarchy of Threads

Host	Ļ					
Root-Group	Ļ					
Work-Group	Ļ		Ļ		Ļ	
Sub-Group	ţ	Ļ	Ļ	Ļ	Ļ	Ļ
Work-Item		$\downarrow \downarrow $			$\downarrow \downarrow $	

- The host has at least one thread and creates one thread per root-group.
- The **root-group** creates one thread per **work-group**.
- Each work-group creates one thread per sub-group.
- Each **sub-group** creates one thread per **work-item**.

Each thread blocks with forward progress guarantee delegation on its children.

New Mental Model: Mapping to OpenCL 1.x

Host	Concurrent	
Root-Group	Weakly Parallel	
Work-Group	Weakly Parallel	
Sub-Group	Weakly Parallel	
Work-Item	Weakly Parallel	

- → <u>At least one</u> {root-group, work-group, sub-group, work-item} makes progress.
 - Individual {root-group, work-group, sub-group, work-item}s have no guarantees.

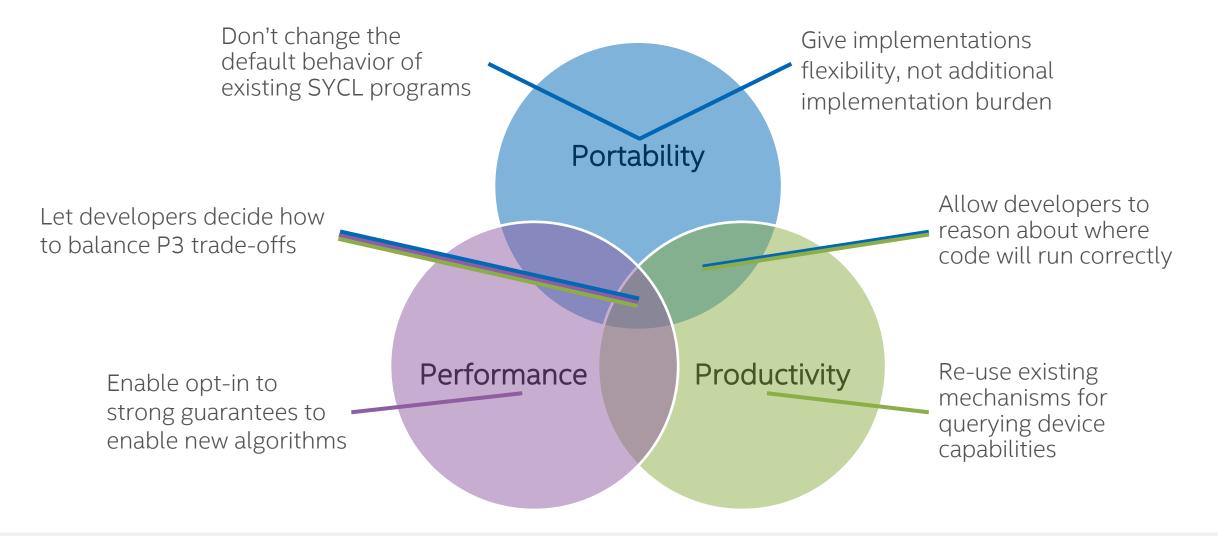
Each thread blocks with forward progress guarantee delegation on its children.

New Mental Model: Mapping to OpenCL 2.x[†]

Host	Concurrent
Root-Group	Weakly Parallel
Work-Group	Weakly Parallel
Sub-Group	<u>Concurrent</u>
Work-Item	Weakly Parallel

- At least one {**root-group**, **work-group**} makes progress.
- Every sub-group in an executing work-group makes progress.
- At least one **work-item** per **sub-group** makes progress.
- Individual {root-group, work-group, work-item}s have no guarantees.

Designing an Extension: High-Level Goals



Using the Extension: Declaring Requirements

struct MyKernel

```
// Kernel function calls arrive_and_wait
// (Other functionality omitted)
void operator()(sycl::nd_item<1> it) {
    ...
    arrive_and_wait(num_work_groups, it.get_group());
    ...
}
```

```
// Kernel Properties: Declare requirements
auto get(sycl::properties tag)
```

```
return sycl::properties {
   sycl::work_group_progress
    <sycl::forward_progress_guarantee::concurrent,
     sycl::execution_scope::root_group>
};
```

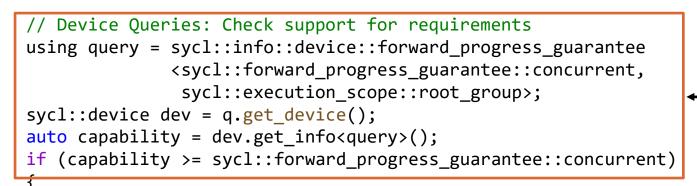
Requirements are embedded in the kernel.

"At least one work-item in each work-group
created by the same root-group must provide concurrent forward progress guarantees."

```
size_t num_work_groups;
};
```

Using the Extension: Submitting the Kernel

Using the Extension: Querying Support



Check whether the device can satisfy the requirements at all.

```
// Kernel Launch Queries: Determine valid ND-range size
using size_query = sycl::info::kernel::max_work_group_size;
using num_query = sycl::info::kernel::max_num_work_groups;
auto bundle = sycl::get_kernel_bundle(q.get_context());
auto kernel = bundle.get kernel<class MyKernel>();
auto wg_size = kernel.get_info<size_query>(q);
auto num_wg = kernel.get_info<num_query>(q, wg_size);
```

Check for implementation-specific limits on the maximum work-group size and number of work-groups.

// Kernel Launch: Use results from queries
auto range = sycl::nd_range<1>{num_wg * wg_size, wg_size};
q.parallel_for(range, MyKernel(num_wg));

else { ... } // Fallback path as before (see previous slides)

Summary

- We've bridged the gap between SYCL and C++17 parallelism
 - Fixed underdefined aspects of SYCL by reusing proven terminology/concepts
 - Defined a way to reason about hierarchical forward progress guarantees
 - Proposed new features to state assumptions/requirements and query support
- Ongoing effort to maintain alignment and influence other standards
 - Explore interaction between ND-range kernels and **std::execution**
 - Apply our learnings to OpenCL, SPIR-V, Vulkan
 - Feedback welcome at <u>https://github.com/intel/llvm/pull/7598</u>

Disclaimers & Notices

Intel technologies may require enabled hardware, software or service activation.

No product or component can be absolutely secure.

Your costs and results may vary.

© Intel Corporation. Intel, the Intel logo, and other Intel marks are trademarks of Intel Corporation or its subsidiaries. Other names and brands may be claimed as the property of others.

Khronos[®] is a registered trademark and SYCL[™] and SPIR[™] are trademarks of The Khronos Group Inc.

Code included in this presentation is licensed subject to the Zero-Clause BSD open source license (OBSD), <u>https://opensource.org/licenses/OBSD</u>

#