Heterogeneous Active Messages (HAM) — Implementing Lightweight Remote Procedure Calls in C++

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Distributed Algorithms and Supercomputing

1/25

You may remember me from such events as SC, ISC, IPDPS, or IXPUG ...

What I do at ZIB:

- **HPC-related** computer science research
 - programming models
 - performance and portability
- development of scientific codes
- user training/consulting for the HLRN supercomputer
- evaluation of upcoming HPC technologies





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- Who is familiar with RPCs/RMIs?
- Who is familiar with active messages?





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Foundation for an efficient and flexible C++ offloading framework.

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Why?

- Foundation for an efficient and flexible C++ offloading framework.
- Target all architectures that can run a process and communicate somehow over an accessibe API.
 - includes CPUs, Xeon Phi accelerators, NEC Vector Engine, ...
 - excludes direct support for current GPUs

User Perspective

What we want:

• execute some function in the address space of a remote process

```
int fun(int a, int b) {
    return a + b;
}
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```

• with something as close as possible to std::async:

```
int main() {
   int a, b; // init somehow

   // run asynchronously
   auto res_future =
        std::async( fun, a, b);
   int c = res_future.get();
}
```

User Perspective

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• for an RPC we need a target process, and some kind of closure to transfer:

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Heterogeneous Active Messages (HAM):

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Heterogeneous Active Messages (HAM):

- enable a similar approach for differing, i.e. heterogeneous binaries
- e.g. by an efficient addresses translation mechanism

Problem: the RPC mechanism

- a) kernel code deployment
- b) efficient kernel invocation

Solution:

- a) symmetric execution model:
- build heterogeneous binaries from same source
- b) Heterogeneous Active Messages
- provide code address translation between heterogeneous processes in O(1)
- use the C++ type-system to:
 - generate message handlers
 - build translation data structures

HAM

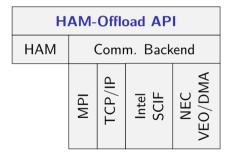
НАМ	Comm. Backend			
	MPI	TCP/IP	Intel SCIF	NEC VEO/DMA

Problem:

 generic means to transfer Heterogeneous Active Messages and data

Solution:

- an abstract Communication Backend
- direct data transfers between offload targets
- implemented for different technologies



Problem:

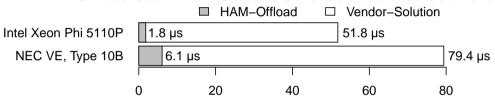
unified API for intra- and inter-node offloading

Solution:

- HAM-Offload C++ API
- offload primitives built on top of HAM and the communication back-end
- light-weight runtime for message execution
- similar functionality as vendor solutions

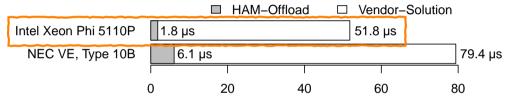
Cost for offloading an empty kernel, i.e. the minimal overhead:

Offload Cost: HAM-Offload vs. Vendor-Provided Solutions



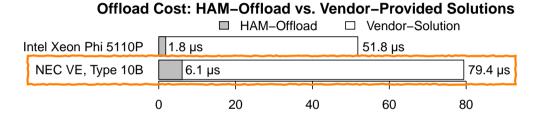
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- ... vs. Intel LEO (pragma-based compiler extension)
 - 28.6× speed-up, i.e. 96.5 % overhead reduction

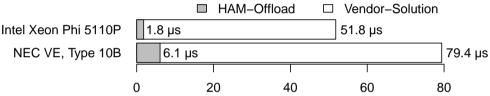
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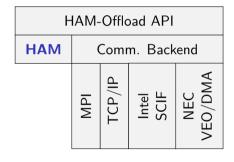
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- ⇒ while being language-only and high-level



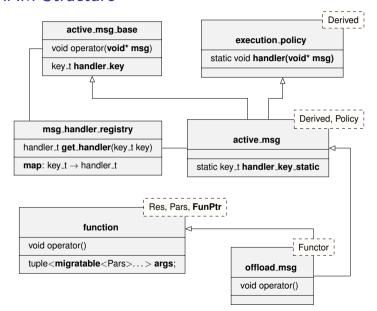
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HAM Structure



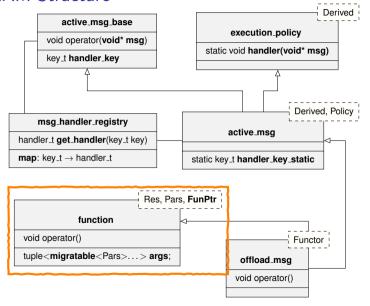
HAM RPC at Runtime

```
offload asynchronously
    auto res_future =
       ham::async(target, f2f(&fun, a, b));
                                     ham::async()
⋖
     function
               f2f()
                       ham::function
                                                  offload msg
Process
     + args
                                                  send()
                                                 receive buffer
                                       reinterpret_cast<>()
            operator()()
                                       handler()
    result
                        offload_msg
                                                active_msg_base
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HAM Structure



function functor

- generated by f2f
- function signature as template type parameter
- function address as template value parameter

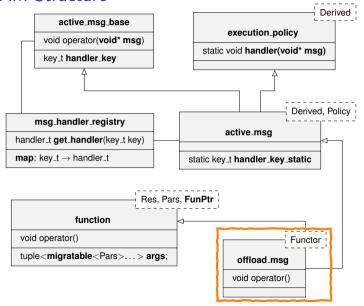
migratable wrapper

- hooks for serialisation/deserialisation
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            operator()()
                                       handler()
                        offload_msg <
    result |
                                               active_msg_base
```

HAM Structure



offload_msg

- inherits a function instantiation
- inherits from active_msg, passing its type upwards (CRTP)
- just an example of how HAM is used in HAM-Offload

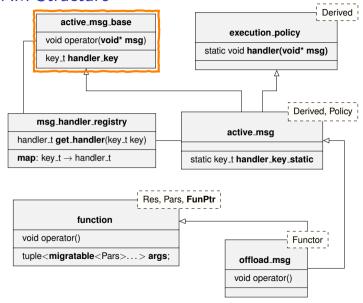
HAM RPC at Runtime

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Process
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            operator()()
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                       offload msg
    result ◀
                                               active msq base
```

Receving side:

- typeless buffer
 - all messages inherit from active_msg_base
- can be called with the receive buffer

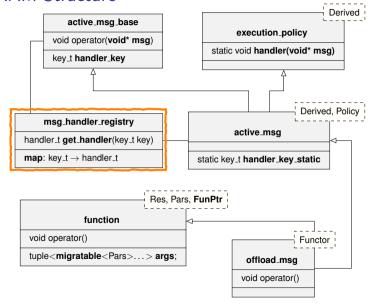
HAM Structure



active_msg_base

- trivial, callable base class
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${\tt msg_handler_registry}$

• LUT: handler key to local function address in O(1)

HAM Structure Derived active_msg_base execution_policy void operator(void* msq) static void handler(void* msg) key_t handler_key Derived, Policy msg_handler_registry active_msq handler_t get_handler(key_t key) **map**: $key_t \rightarrow handler_t$ static kev_t handler_kev_static Res. Pars. FunPtr function void operator() tuple<migratable<Pars>...> args; offload msg void operator()

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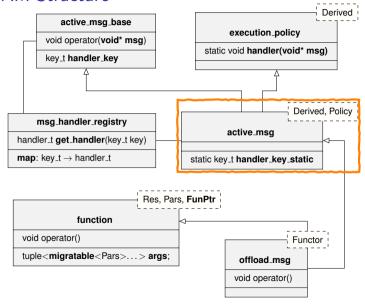
${\tt msg_handler_registry}$

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execution_policy

- the actual handler
- **upcasts** to Derived

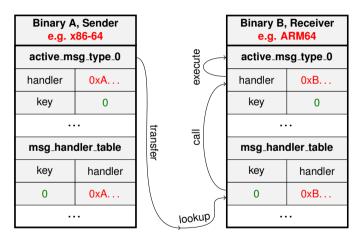
HAM Structure



active_msg

- links the message type to its handler key, i.e. O(1) look-up
- static member init. provides hook for collecting handler addresses prior to main
- ⇒ collect addresses and typeid().name()

HAM Address Translation



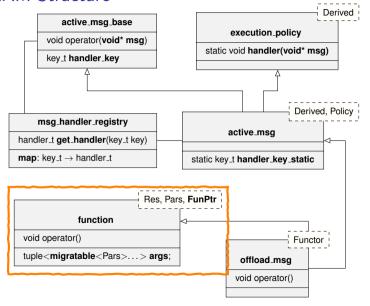
- keys are valid across binaries, addresses are not
- keys are defined by the lexicographical order of the message-type's typeid names
- ⇒ coordination of global keys without communication
 - requires compatible C++ ABIs across compilers (icc, clang, gcc, ncc) and platforms (x86, KNC/KNL, VE, ARM)
- ⇒ most ABIs refer to the IA-64 C++ ABI for the relevant parts

Handler Maps and C++ RTTI Names

```
========= BEGIN HANDLER MAP =======
typeid name:
 N3ham3msg10active_msgINS_7offload6detail11offload_msgINS2_7runtime17
 terminate_functorENSO_23execution_policy_directEEES7_EE
handler address: 0x440d10
typeid_name:
 N3ham3msg10active_msgINS_7offload6detail18offload_result_msgINS_
 8functionIPFiiEXadL_ZZ13ham_user_mainiPPcEN3$_08
    __invokeEiEEEENSO_24default_execution_policyEEESC_EE
handler_address: 0x42a7e0
typeid_name:
 N3ham3msg10active msgINS 7offload6detail18offload result msgINS
 8functionIPFvvEXadL Z7
    fun_onevEEEENSO_24default_execution_policyEEES9_EE
handler address: 0x42db20
index: 0, handler address: 0x440d10
index: 1,
              handler_address: 0x42a7e0
index: 2, handler_address: 0x42db20
```

```
// function signature as template type parameter
// function pointer as template value parameter
template < typename Result, typename ... Pars,
          Result (*FunctionPtr)(Pars...)>
class function < Result (*)(Pars...), FunctionPtr > {
public:
    // variadic constructor template
    // takes compatible argument types
    template < typename . . . Args >
    function(Args&&... arguments);
    Result operator()() const;
private:
    std::tuple<migratable<Pars>...> args;
};
```

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Cumbersome instantiation:
     function < decltype (fun ptr), fun ptr > (/* arguments */);
Hence the f2f (variadic macro):
     // f2f = "function to functor"
     // NOTE: the '&' is required
     f2f(&fun. /* arguments */);
```

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// function signature as template type parameter
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     class function < Result (*)(Pars...), FunctionPtr > { ... };
Cumbersome instantiation:
     function < decltype (fun_ptr), fun_ptr > (/* arguments */);
Hence the f2f (with C++17):
    template < auto fun ptr >
     using f2f = function < decltype (fun ptr), fun ptr>;
     // C++17 f2f syntax:
     // NOTE: the '&' before fun can be skipped
    f2f < fun > (/* arguments */):
```

So what about Lambdas?

- capturing lambdas are not tractable as their state is inaccessible
- captureless lambdas have an implicit conversion operator to function pointer, which is constexpr since C++17
 - ⇒ can be used as template value argument

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Requires a little convincing, though:

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The '+' can be somewhat hidden:

```
// lambda to function (L as type argument)
template < typename L, typename Args...>
auto l2f(L lambda, Args&&... args) {
    // conversion to pointer through +
    return f2f < + lambda > (std::forward < Args > (args)...);
}
// resulting syntax:
l2f([](/* Pars */){ /* do sth. */ },
    /* args */);
```

Final syntaxes:

```
// some offloaded function
int square(int x) {
    return x * x;
// offload functor, f2f as macro (pre C++17)
offload::async(target, f2f(&square, 42));
// offload functor, f2f auto template (C++17)
offload::async(target, f2f <square > (42));
// offload anonymous lambda (C++17)
offload::async(target, 12f([](int x) { return x * x; },
                           42));
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 - ⇒ review and reduce
- ABI, RTTI, types like long double, ...
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 - ⇒ ensure **compiler interoperability** of (new) features

Seemingly incompatible features:

- complex, compiler-generated code, e.g. from lambda expressions
 - ⇒ take distributed/heterogeneous systems into account

EoP

Thank you.

Feedback? Questions? Ideas?

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https://github.com/noma/ham