

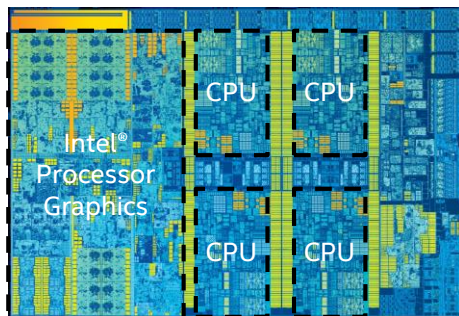


# INTEGRATING OPENCL™ KERNELS INTO THE PROGRAM FLOW

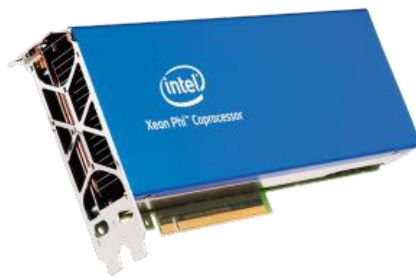
Alexei Katranov

IWOCL '16, April 21, 2016, Vienna, Austria

# Hardware: customization, integration, heterogeneity



Multicore CPU +  
integrated units for  
graphics, media and  
compute



Discrete co-processors  
and accelerators



FPGAs, fixed function  
devices, domain-specific  
compute engines, etc...

Diverse and heterogeneous environments  
with multiple compute resources

# Intel® Threading Building Blocks (Intel® TBB)

- Widely used C++ template library
- Rich feature set for general purpose parallelism
- For Windows\*, Linux\*, OS X\*, Android\*, etc.
- Both commercial and open-source licenses
- Commercial support for Intel® Atom™, Core™, Xeon® processors, and for Intel® Xeon Phi™ coprocessors
- Community contributions for non-Intel architectures



<http://software.intel.com/intel-tbb>

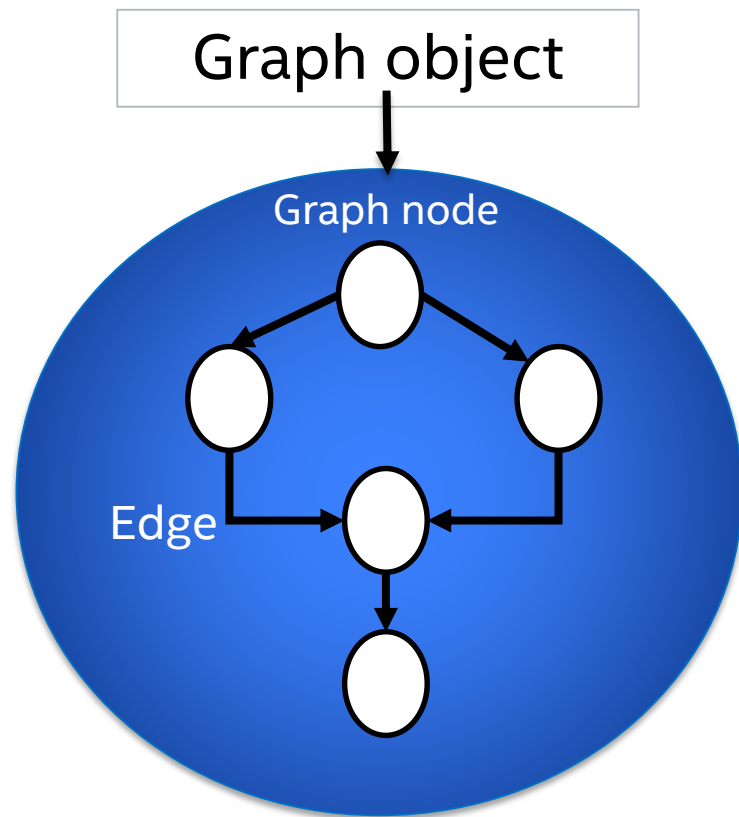
<http://threadingbuildingblocks.org>

# Rich Feature Set for Parallelism

|  |  |   |                        |   |
|--|--|---|------------------------|---|
| <h2>Generic Parallel Algorithms</h2>   | <h2>Flow Graph</h2>  | <h2>Concurrent Containers</h2>  |                        |   |
| <p>Efficient scalable way to exploit the power of multi-core without having to start from scratch</p>                    | <p>A set of classes to express parallelism as a graph of compute dependencies and/or data flow</p> | <p>Concurrent access, and a scalable alternative to serial containers with external locking</p> |                        |   |
|  |  | <h2>Synchronization Primitives</h2>   |                        |   |
| <h2>Task Scheduler</h2> <p>Sophisticated work scheduling engine that empowers parallel algorithms and the flow graph</p> |  | <h2>Thread Local Storage</h2>   | <h2>Threads</h2>       | <h2>Miscellaneous</h2>                          |
|  |  | <p>Unlimited number of thread-local variables</p>   | <p>OS API wrappers</p> | <p>Thread-safe timers and exception classes</p> |
| <h2>Memory Allocation</h2>   |  |   |                        |   |
| <p>Scalable memory manager and false-sharing free allocators</p>   |  |   |                        |   |

# Intel TBB Flow Graph at glance

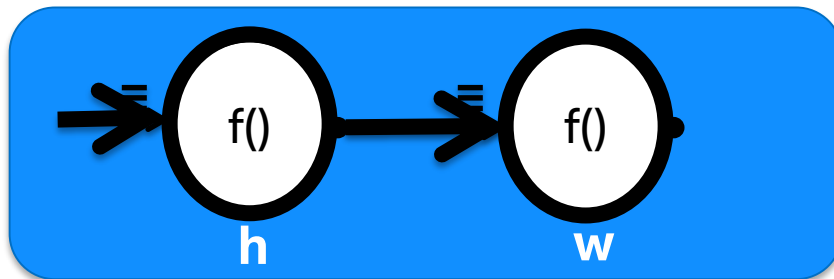
- Intel TBB Flow Graph is an abstraction built on top of TBB task scheduler API
  - Like an additional programming model
  - Explicitly defined control and data dependencies between computations
  - Parallelism is automatically extracted
- Intel TBB flow graph is targeted to multicore shared memory systems.



# Hello World Example

Users create nodes and edges, interact with the graph and wait for it to complete

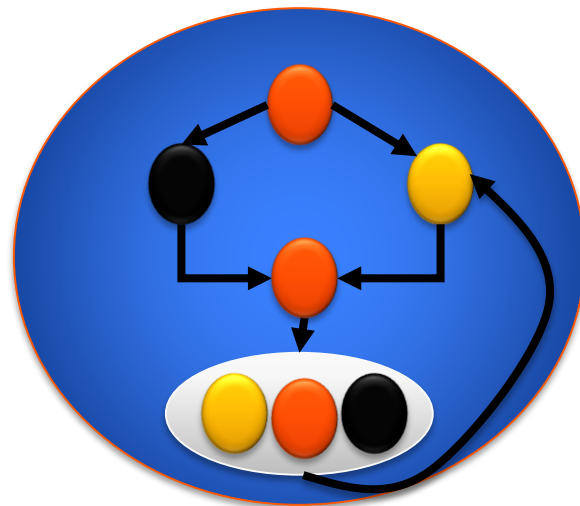
```
tbb::flow::graph g;  
tbb::flow::continue_node< tbb::flow::continue_msg >  
  h( g, []( const continue_msg & ) { std::cout << "Hello "; } );  
tbb::flow::continue_node< tbb::flow::continue_msg >  
  w( g, []( const continue_msg & ) { std::cout << "World\n"; } );  
tbb::flow::make_edge( h, w );  
h.try_put(continue_msg());  
g.wait_for_all();
```



**COMBINING OPENCL™ AND INTEL TBB**

# Idea of Heterogeneous Flow Graph

- TBB flow graph as a coordination layer
- Be the glue that connects hetero HW and SW IP together
- Expose parallelism between blocks; simplify integration



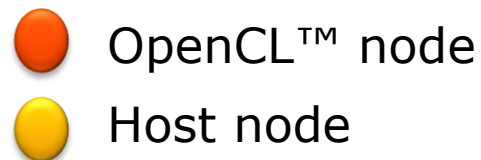
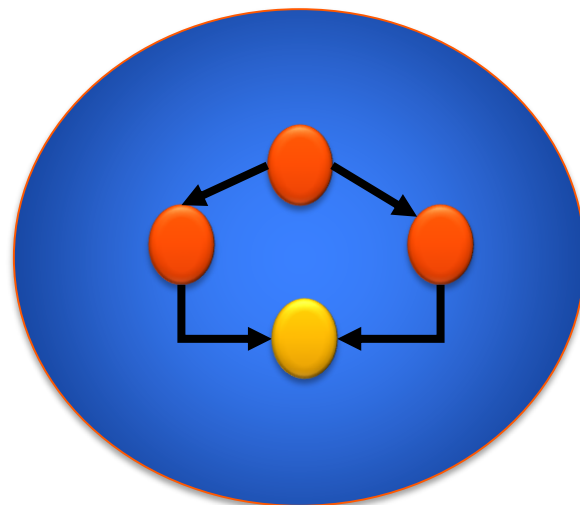
- Device 1
- Device 2
- Device 3



# OpenCL™ node

## Core functionality:

- enumerate & query OpenCL™ devices
- select a device to be used for program execution
- transfer data to/from the device
- execute a given kernel there
- support efficient kernel chaining (no excessive data transfer)



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# Hello World example for OpenCL node

```
// A graph with OpenCL support.
opengl_graph g;

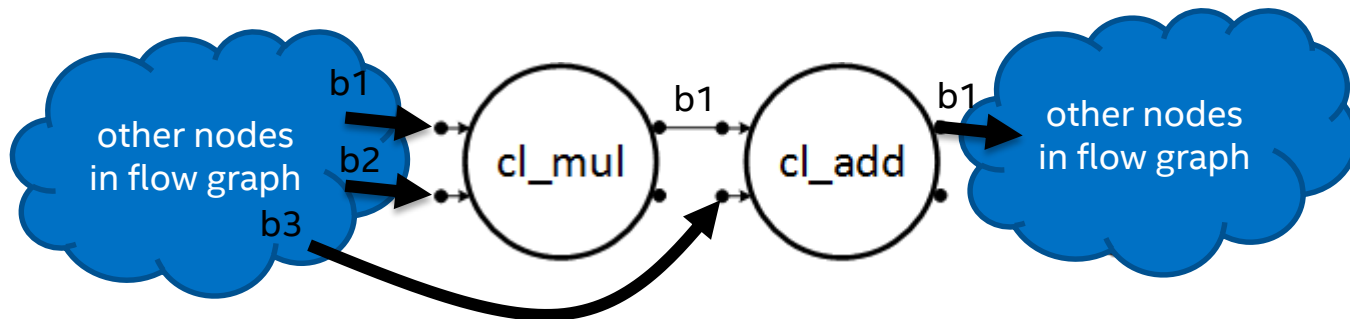
const char str[] = "Hello, World!";
// OpenCL buffer for the string
opengl_buffer<cl_char> b(g, sizeof(str));
// Copy the string to the buffer
std::copy_n(str, sizeof(str), b.begin());

// A node that outputs the content of an incoming buffer
opengl_node<tuple<opengl_buffer<cl_char>>> clPrint(g, "hello_world.cl", "print");
k.set_ndranges({1});

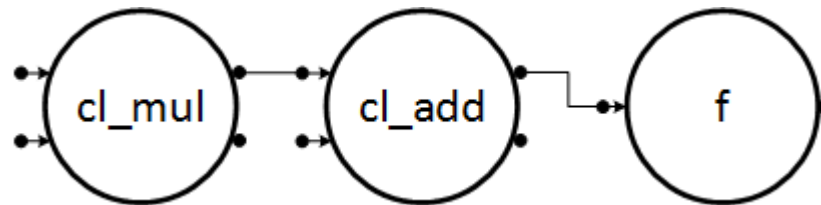
// Send the buffer as the node input
input_port<0>(clPrint).try_put(b);
// Wait for work completeion.
g.wait_for_all();
```

```
// hello_world.cl
kernel void print( global char *str ) {
    printf("OpenCL says '");
    for ( ; *str; ++str ) printf("%c", *str);
    printf("'\\n");
}
```

# OpenCL node pipeline example



# OpenCL node pipeline example



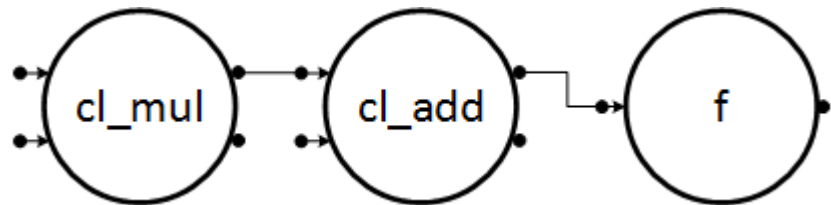
```
typedef opencl_buffer<cl_int> cl_buffer_t;
typedef opencl_node < tuple<cl_buffer_t, cl_buffer_t> > cl_node_t;

// Create nodes
cl_node_t cl_mul( g, "program.cl", "mul" );
cl_node_t cl_add( g, "program.cl", "add" );
function_node_t f( g, unlimited, []( const cl_buffer_t &t ) {...} );

// Create dependencies between nodes
make_edge( cl_mul, cl_add );
make_edge( cl_add, f );

// Put buffers to the graph
cl_buffer_t b1( g, N ), b2( g, N ), b3( g, N );
input_port<0>( cl_mul ).try_put( b1 );
input_port<1>( cl_mul ).try_put( b2 );
input_port<1>( cl_add ).try_put( b3 );
```

# Under the hood



## Your code

```
cl_node_t cl_mul
```

## Real work

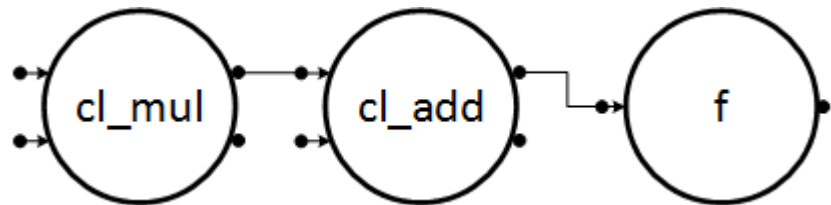
### OpenCL initialization

1. Query the available devices
2. Create context
3. Create queue

### Create a kernel:

1. Prepare the list of devices
2. Read file
3. Prepare program
4. Build program
5. Print error if observed
6. Get a kernel

# Under the hood



## Your code

```
cl_node_t cl_mul
```

```
cl_node_t cl_add
```

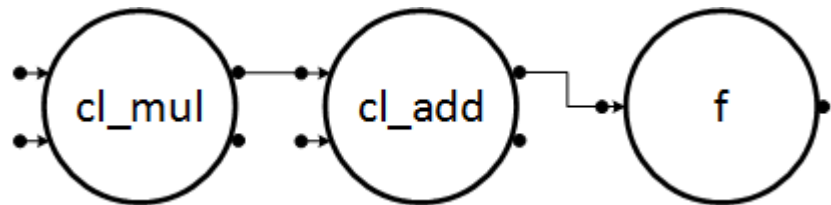
## Real work

```
OpenCL initialization
```

```
Create a kernel
```

```
Create a kernel
```

# Under the hood



## Your code

```
cl_node_t cl_mul
```

```
cl_node_t cl_add
```

```
cl_buffer_t b1, b2, b3
```

## Real work

```
OpenCL initialization
```

```
Create a kernel
```

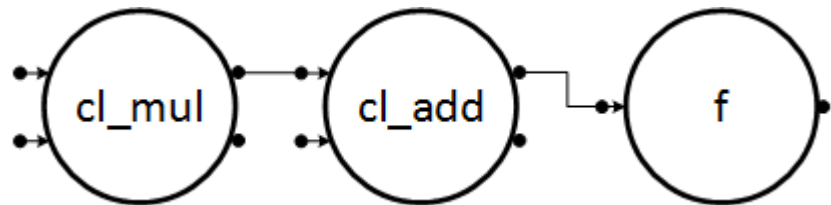
```
Create a kernel
```

```
Create a buffer
```

```
Create a buffer
```

```
Create a buffer
```

# Under the hood



## Your code

```
cl_node_t cl_mul
```

```
cl_node_t cl_add
```

```
cl_buffer_t b1,b2,b3
```

```
cl_mul<0>.put( b1 )
```

## Real work

```
OpenCL init
```

```
Kernel x2
```

```
Buffer x3
```

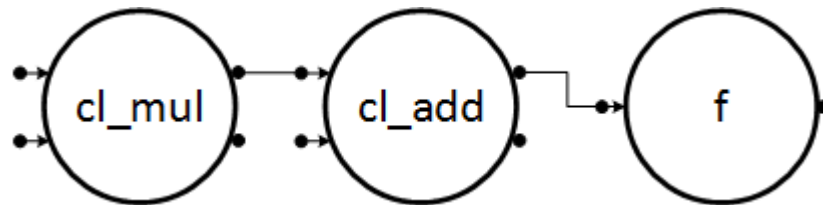
```
Send msg
```

## Work in parallel

```
Move data to device
```



# Under the hood



## Your code

...

```
cl_mul<0>.put( b1 )
```

```
cl_mul<1>.put( b2 )
```

## Real work

...

Send msg

Send msg

## Work in parallel

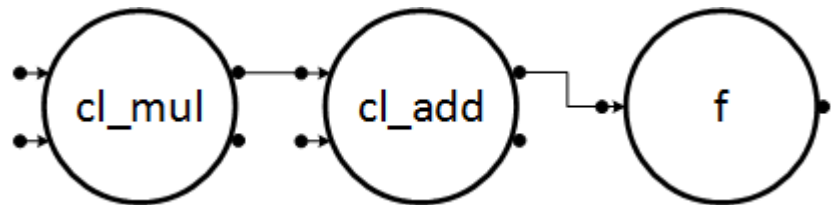
Move data to device

Move data to device

Run kernel "cl\_mul"

1. Set arguments
2. Enqueue kernel
3. Put "b1" to "cl\_add"

# Under the hood



## Your code

...

```
cl_mul<1>.put( b2 )
```

```
cl_add<1>.put( b3 )
```

## Real work

...

Send msg

Send msg

## Work in parallel

Move data to device

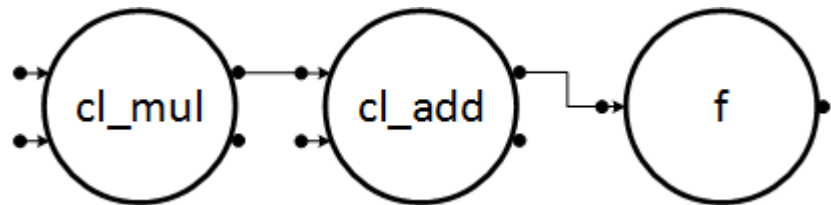
Run kernel "cl\_mul"

Move data to device

Sync with previous kernel  
"cl\_mul"

Run kernel "cl\_add"

# Under the hood



## Your code

...

```
cl_add<1>.put( b3 )
```

## Real work

...

Send msg

## Work in parallel

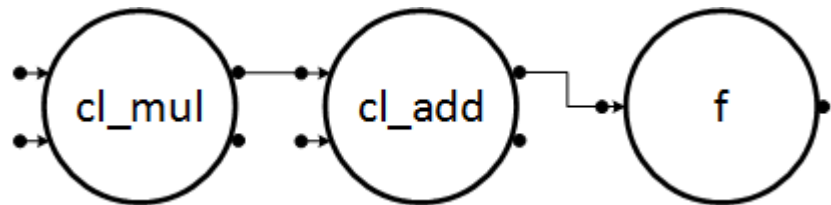
Move data to device

Sync with previous kernel  
"cl\_mul"

Run kernel "cl\_add"

Sync with previous kernel  
"cl\_add"

# Under the hood



## Your code

...

```
cl_add<1>.put( b3 )
```

## Real work

...

Send msg

## Work in parallel

Move data to device

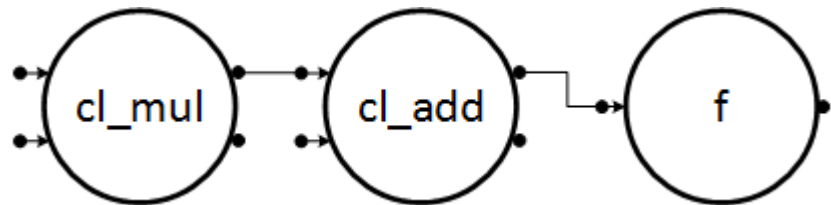
Sync with previous kernel  
"cl\_mul"

Run kernel "cl\_add"

Sync with previous kernel  
"cl\_add"

Move data and run "f"

# Under the hood



## Your code

```
cl_node_t cl_mul
```

```
cl_node_t cl_add
```

```
cl_buffer_t b1, b2, b3
```

```
cl_mul<0>.put( b1 )
```

```
cl_mul<1>.put( b2 )
```

```
cl_mul<2>.put( b3 )
```

## Real work

### OpenCL initialization

#### Create a kernel:

1. Prepare the list of devices
2. Read file
3. Prepare program
4. Build program
5. Print error if observed

#### Create buffer

Send message

Send message

Send message

## Work in parallel

### Move data to device

#### Run kernel "cl\_mul"

#### Run kernel "cl\_add"

1. Set arguments
2. Enqueue kernel
3. Put "b1" to "cl\_add"

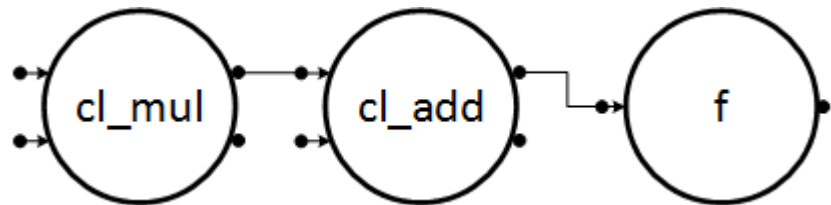
#### Sync with previous kernel

#### Run kernel "cl\_add"

#### Sync with previous kernel "cl\_add"

#### Move data and run "f"

# Under the hood



## Your code

```
cl_node_t cl_mul
```

```
cl_node_t cl_add
```

```
cl_buffer_t b1,b2,b3
```

```
cl_mul<0>.put( b1 )
```

```
cl_mul<1>.put( b2 )
```

```
cl_mul<2>.put( b3 )
```

## Real work

OpenCL initialization

Create a kernel:

1. Download
2. Create
3. 1. 5
- 4.
- 5.

Create kernel

Send message

Send message

Send message

## Work in parallel

Move data to device

Move data to device

Move data to device

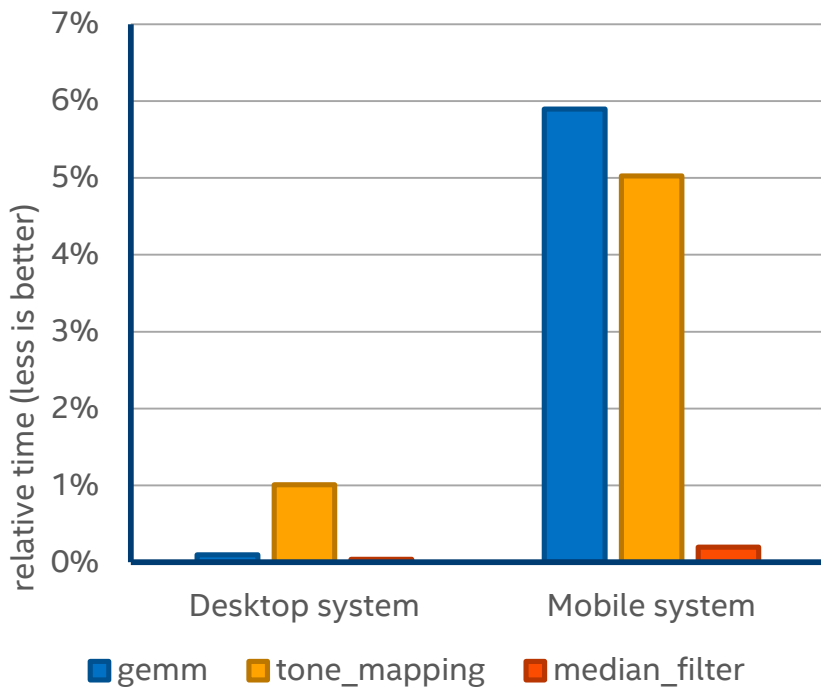
Intel TBB work

Move data and run "f" kernel

Move data and run "f"

# PERFORMANCE EVALUATION

# OpenCL node overheads



## Configuration info:

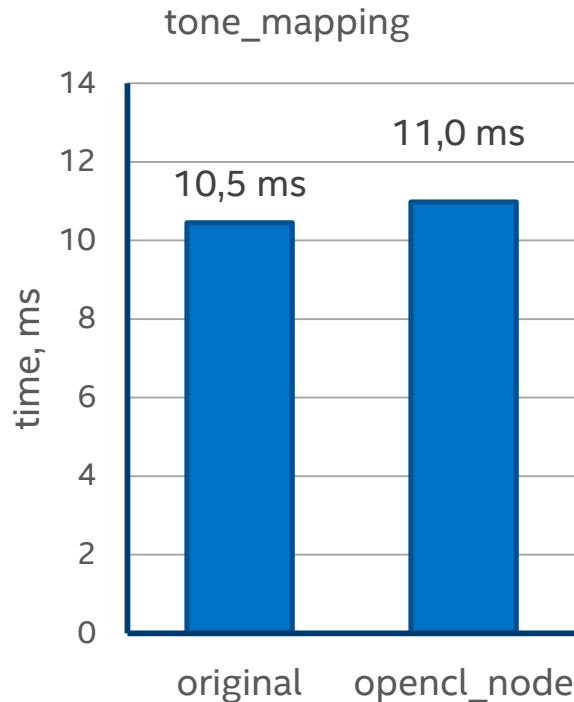
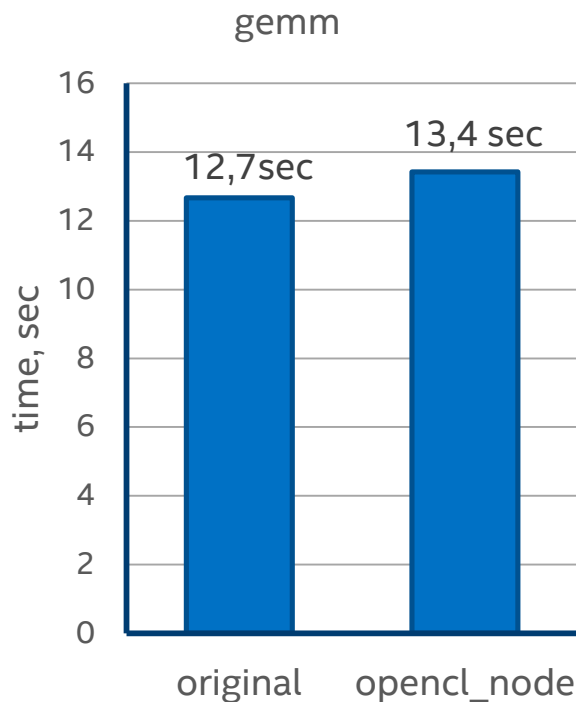
Desktop system: Hardware: Intel® Core™ i7-6700K CPU @4.00Ghz, 16 GB RAM; Software: Microsoft\* Windows 10 Enterprise, Microsoft Visual Studio\* Professional 2015 Update 1, Intel HD Graphics Driver for Windows 15.40.

Mobile system: Intel Core i5-4300U CPU @1.90Ghz, 8 GB RAM; Software: Microsoft Windows 8.1 Enterprise, Microsoft Visual Studio Professional 2015 Update 2, Intel HD Graphics Driver for Windows 15.36

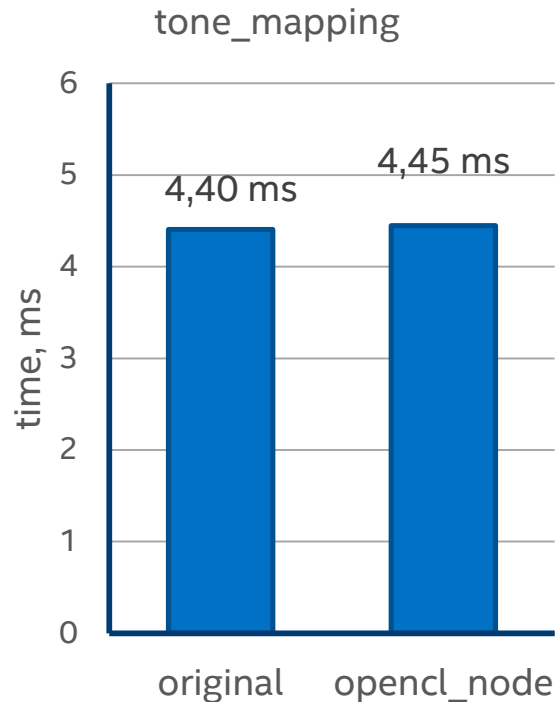


# OpenCL node overheads in detail

## Mobile system



## Desktop system



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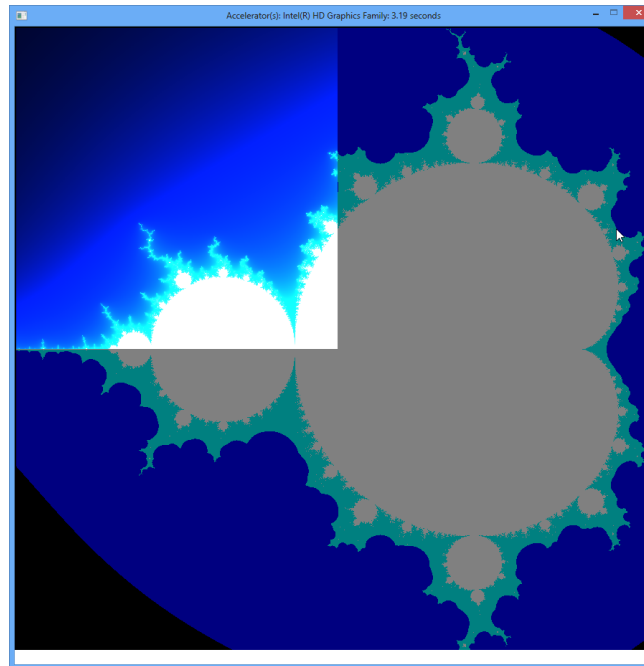
# Load balancing CPU and GPU



OpenCL™ node (CPU)



OpenCL™ node (GPU)



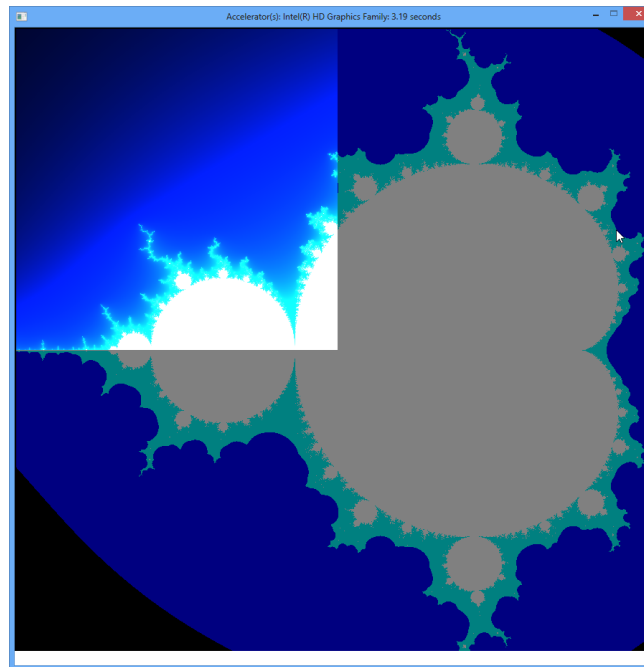
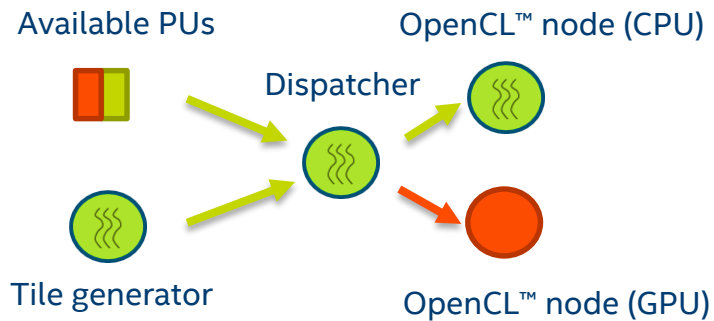
Generic support makes coordinating with any model easier and efficient

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# Load balancing CPU and GPU

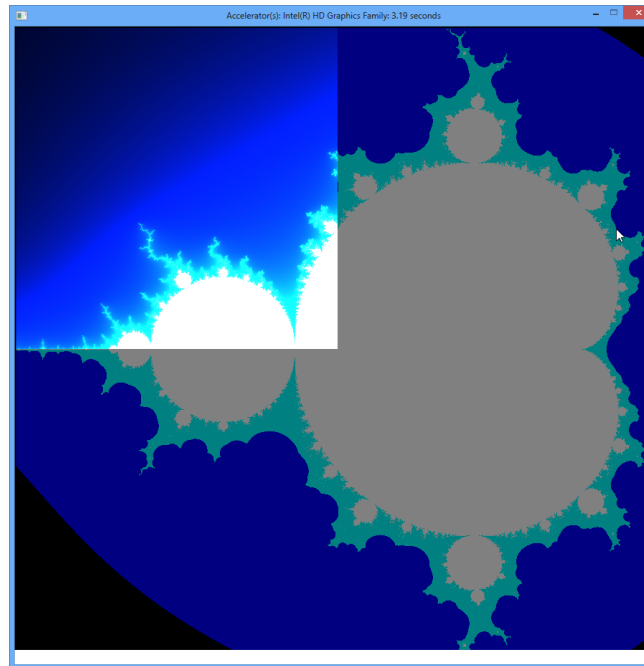
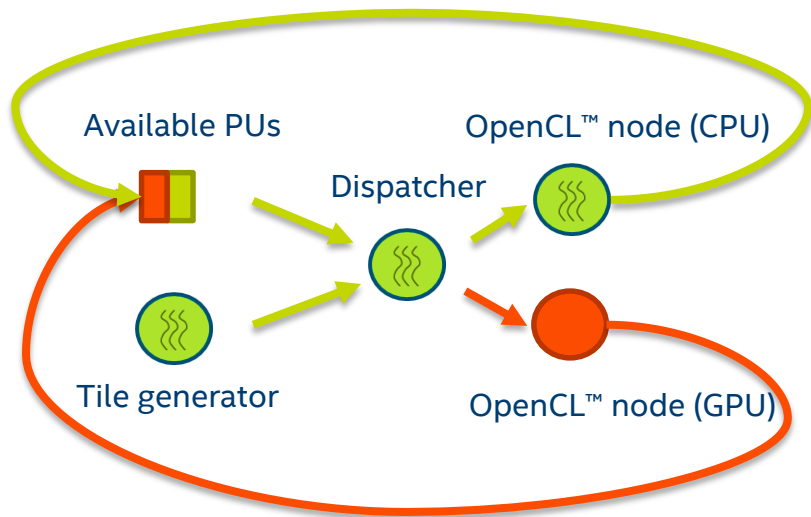


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# Load balancing CPU and GPU

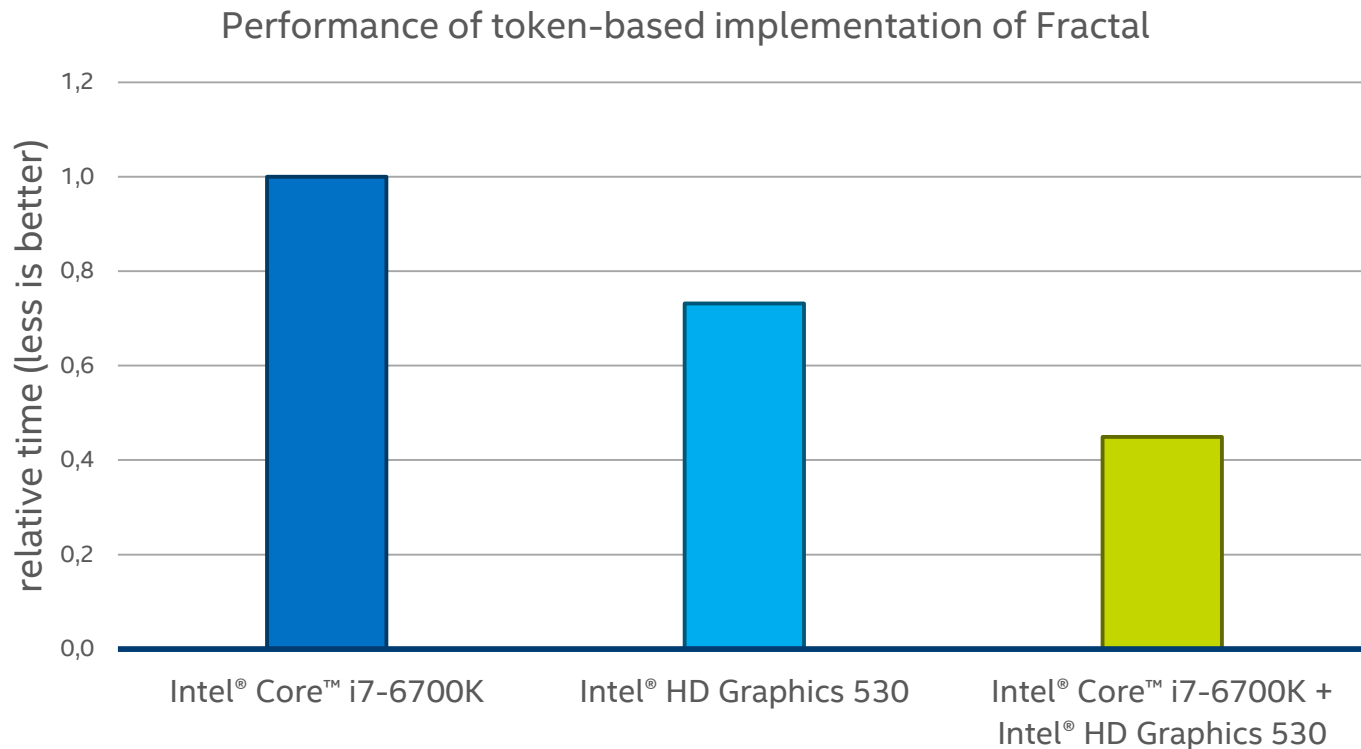


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# Load balancing CPU and GPU: performance



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# SUMMARY

# Summary

Intel TBB flow graph is a coordination layer on heterogeneous systems:

- First class support for OpenCL (opengl\_node overview: <https://software.intel.com/en-us/blogs/2015/12/09/opengl-node-overview>)
- Reasonable performance overheads (about 1% for 4 ms workload on a desktop system)
- Declarative “language” to express unstructured parallelism, e.g. token-based balancing scheme

Intel TBB is open source and freely available on

<https://www.threadingbuildingblocks.org/>

# Acknowledgments

## Our thanks to

- Alexey Kukanov for co-authoring and thorough review
- Michael Voss for material contribution to the features described in this presentation
- Robert Ioffe for evaluating our work and providing valuable feedback
- Others who helped in developing the functionality



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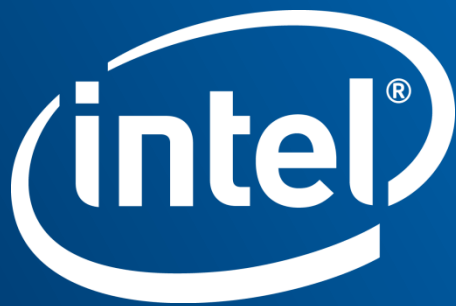
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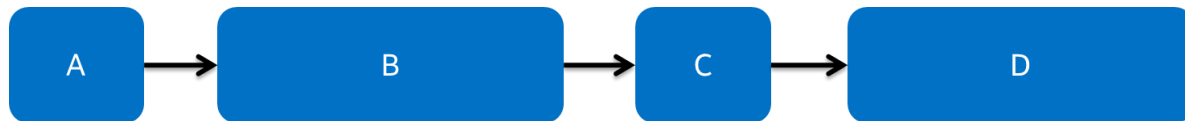
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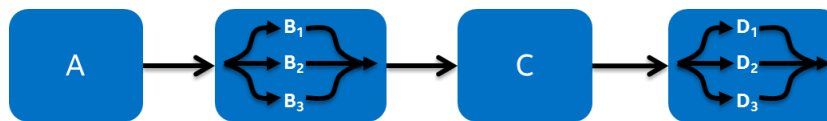
# Backup: Motivation for data flow and graph-parallelism

$\mathbf{x} = \mathbf{A} ();$   
 $\mathbf{y} = \mathbf{B} (\mathbf{x});$   
 $\mathbf{z} = \mathbf{C} (\mathbf{x});$   
 $\mathbf{D} (\mathbf{y}, \mathbf{z});$

Serial implementation (perhaps vectorized)



Loop-parallel implementation



Loop- and graph-parallel implementation

