

IWOCL 2026



OpenCL: State of the Union 2026

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(Representing the Khronos OpenCL Working Group)



Let's think back briefly to 2020...











OpenCL 3.1

- **Raises the Bar for Heterogeneous Compute Acceleration**
 - Incorporates Features with Proven Value, Use, and Support
- **Expecting Wide Adoption in 2026**
 - Including open source and layered implementations
 - Arm, CLVK, Imagination, Intel, Mesa Rusticl, PoCL, Qualcomm, ...
- **Continuing to Innovate via Extensions**
 - Driven by multiple markets: mobile, embedded, desktop, HPC, AI
 - Finalizing: Command Buffers, USM, Cooperative Matrix, more!
 - In development: AI data formats, image tiling controls, more!



Khronos Connects Software to Silicon

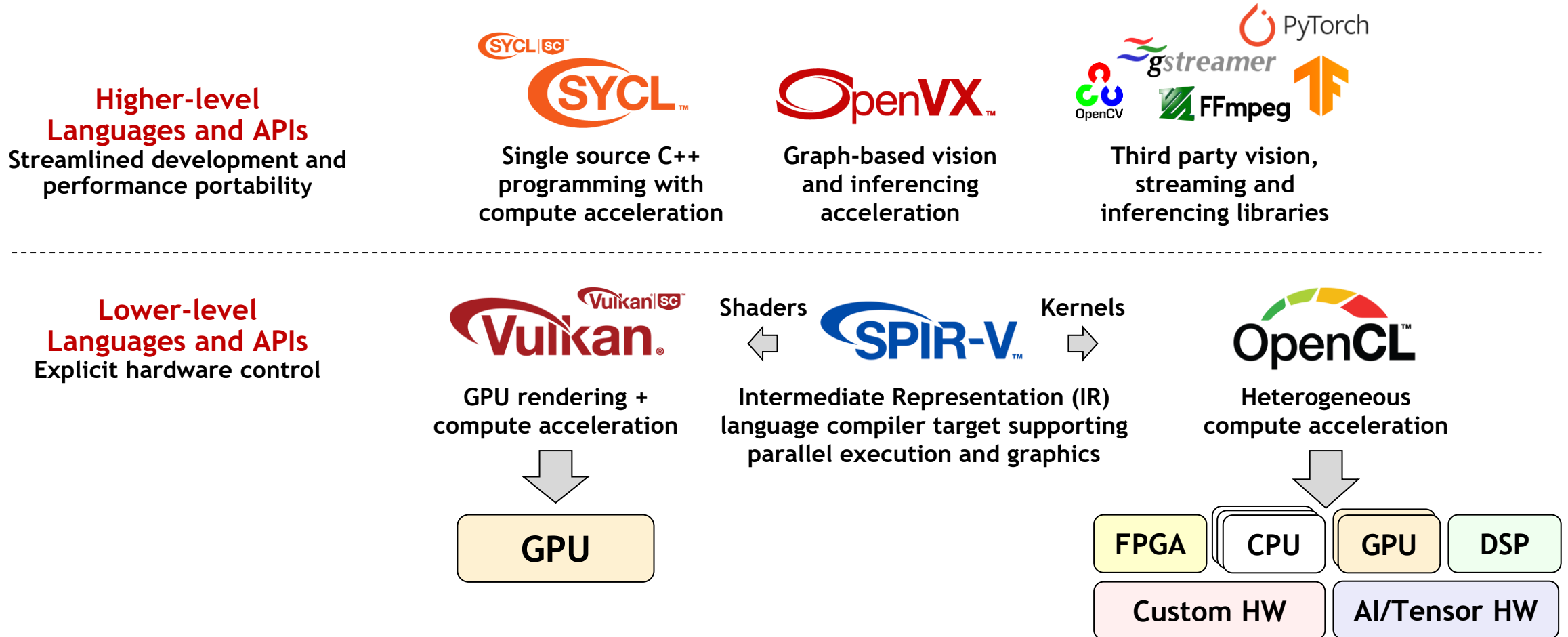


Non-profit Standards Consortium
 creating open, royalty-free standards
 Focused on runtime APIs and file formats
 for 3D, XR, AI, vision, and
 parallel compute acceleration
 Member-driven, open to any company

Founded in 2000
 ~ 135 Members | ~ 40% US, 30% Europe, 30% Asia

Khronos Compute Acceleration

Choice of programming models to meet the needs of diverse developers
Higher-level applications, libraries, and languages often access hardware acceleration through lower-level APIs



OpenCL - Low-level Parallel Programming

Programming and Runtime Framework for Application Acceleration

Offload compute-intensive kernels onto parallel heterogeneous processors
CPUs, GPUs, DSPs, FPGAs, Tensor Processors
OpenCL C or C++ kernel languages

Platform Layer API

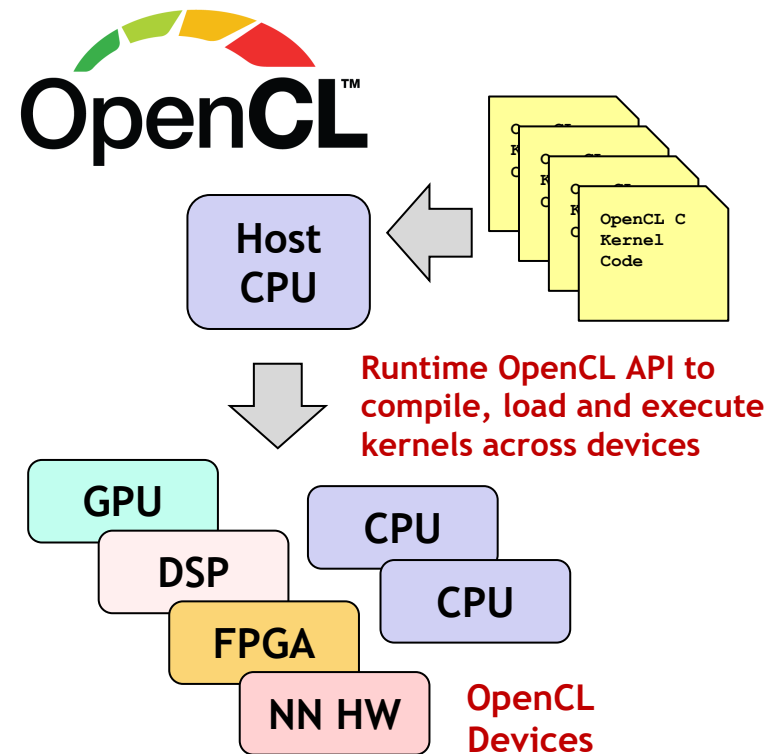
Query, select and initialize compute devices

Runtime API

Build and execute kernels programs on multiple devices

Explicit Application Control

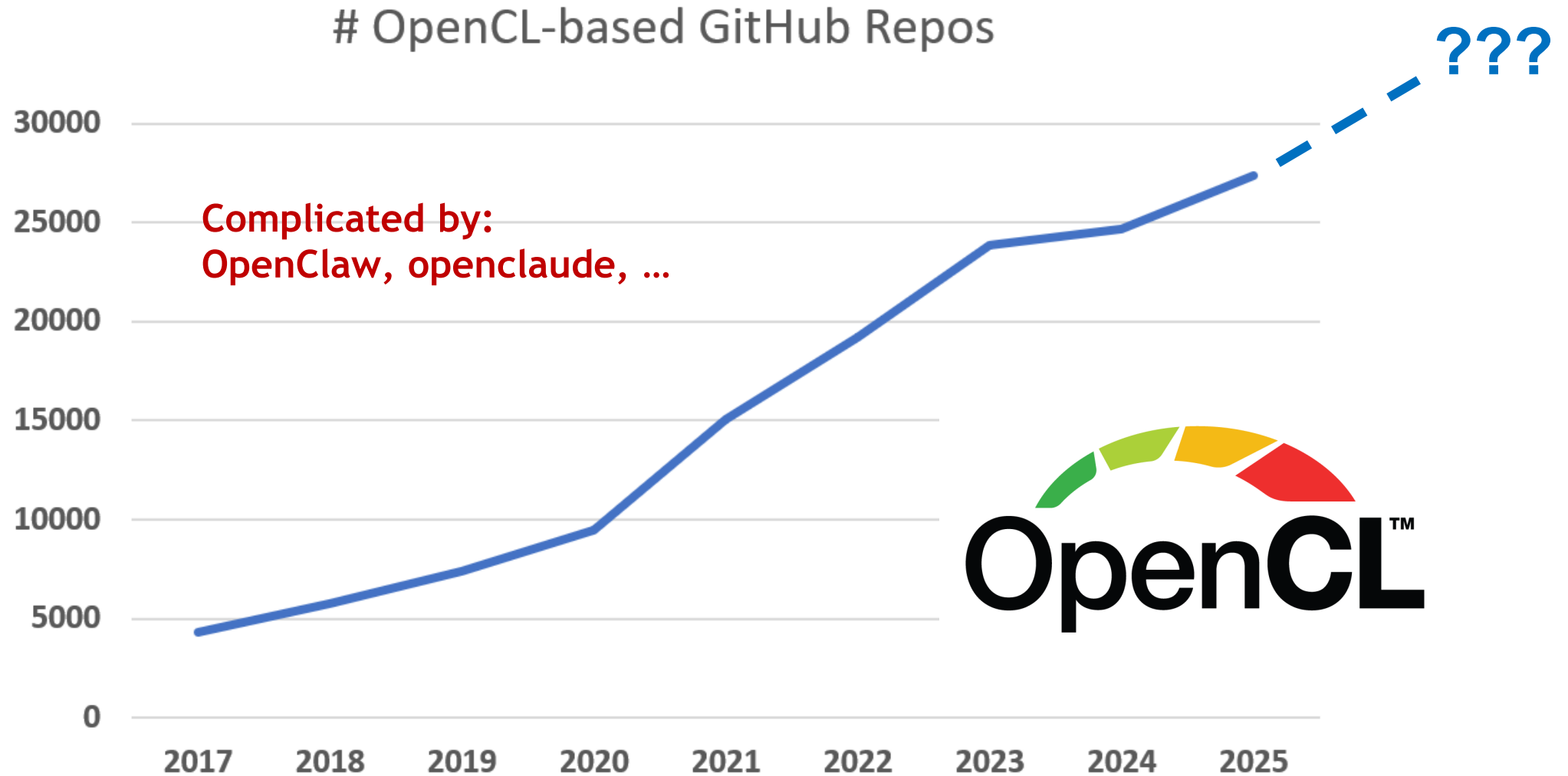
Which programs execute on what device
Where data is stored in memories in the system
When programs are run, and what operations are dependent on earlier operations



Complements GPU-only APIs

Simpler programming model
Relatively lightweight run-time
More language flexibility, e.g., pointers
Rigorously defined numeric precision

OpenCL Open-Source Project Momentum



OpenCL on GPUInfo.org



Home of the community driven hardware databases for Khronos APIs.



OpenGL® is a widely adopted 2D and 3D graphics API available on many desktop platforms. It features hundreds of extensions to support the latest GPU features.



Vulkan is the new generation, open standard API for high-efficiency access to graphics and compute on modern GPUs, available on desktop and mobile platforms.



OpenGL ES is a 2D and 3D graphics API for embedded devices. It's widely used in the mobile space and available on almost any mobile device.



OpenCL™ is an open standard for cross-platform, parallel programming of diverse accelerators found in supercomputers, cloud servers, personal computers, mobile devices and embedded platforms.

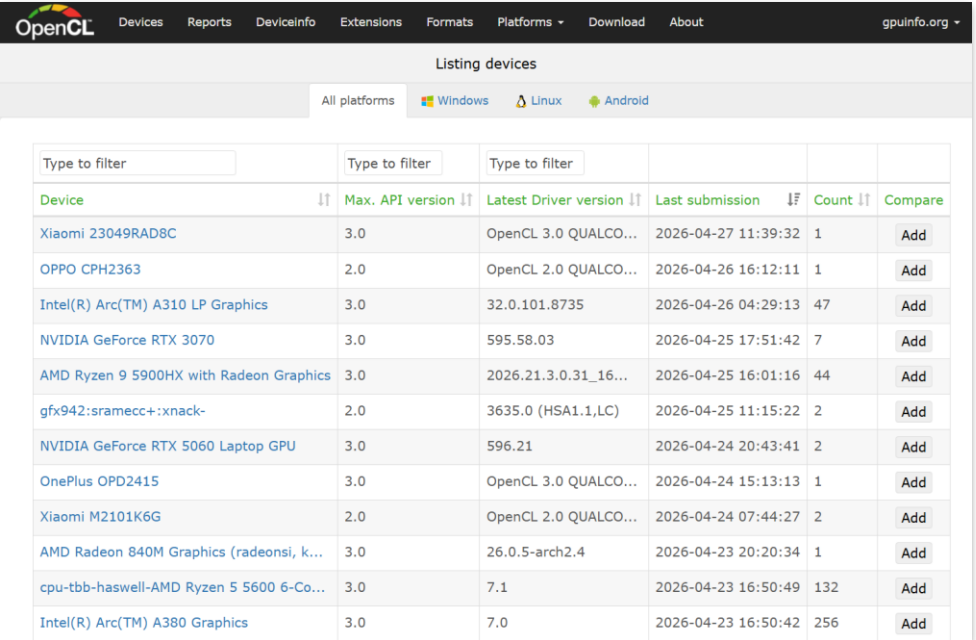
Almost 6700 reports online, 1800 additional reports added in the past year!

The online GPUInfo.org database is populated using the [OpenCL Hardware Capability Viewer](#) application

Available for Windows, Linux and Android

Reads and displays OpenCL information and uploads to the database

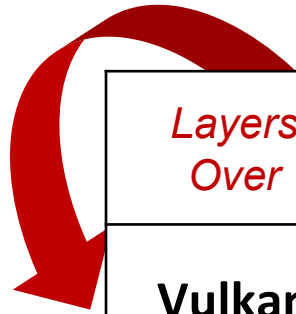
Please download and run to help populate the database!



Type to filter	Type to filter	Type to filter			
Device	Max. API version	Latest Driver version	Last submission	Count	Compare
Xiaomi 23049RAD8C	3.0	OpenCL 3.0 QUALCO...	2026-04-27 11:39:32	1	Add
OPPO CPH2363	2.0	OpenCL 2.0 QUALCO...	2026-04-26 16:12:11	1	Add
Intel(R) Arc(TM) A310 LP Graphics	3.0	32.0.101.8735	2026-04-26 04:29:13	47	Add
NVIDIA GeForce RTX 3070	3.0	595.58.03	2026-04-25 17:51:42	7	Add
AMD Ryzen 9 5900HX with Radeon Graphics	3.0	2026.21.3.0.31_16...	2026-04-25 16:01:16	44	Add
gfx942:sramecc+:xnack-	2.0	3635.0 (HSA1.1,LC)	2026-04-25 11:15:22	2	Add
NVIDIA GeForce RTX 5060 Laptop GPU	3.0	596.21	2026-04-24 20:43:41	2	Add
OnePlus OPD2415	3.0	OpenCL 3.0 QUALCO...	2026-04-24 15:13:13	1	Add
Xiaomi M2101K6G	2.0	OpenCL 2.0 QUALCO...	2026-04-24 07:44:27	2	Add
AMD Radeon 840M Graphics (radeonsi, k...	3.0	26.0.5-arch2.4	2026-04-23 20:20:34	1	Add
cpu-tbb-haswell-AMD Ryzen 5 5600 6-Co...	3.0	7.1	2026-04-23 16:50:49	132	Add
Intel(R) Arc(TM) A380 Graphics	3.0	7.0	2026-04-23 16:50:42	256	Add

API Layering

Enabled by growing robustness of open-source compiler ecosystem using SPIR-V



<i>Layers Over</i>	Vulkan	OpenGL	OpenCL	OpenGL ES	DX12	DX8-11
Vulkan		Zink	clspv + clvk/Angle Rusticl + Zink	Angle GLOVE	vk3d-Proton vk3d	DXVK WineD3D
OpenGL	Ashes			Angle		WineD3D
DX12	Dozen	Microsoft 'GLOn12'	Microsoft 'CLOn12'			Microsoft D3D11On12
DX9-11	Ashes			Angle		
Metal	MoltenVK			Angle MoltenGL		

ROWS Benefit Platforms by adding APIs

COLUMNS Benefit ISVs by making an API available everywhere

Layered OpenCL Implementations

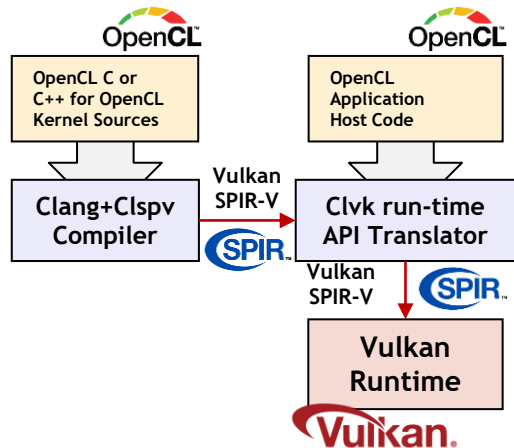
clspv + clvk

OpenCL over Vulkan
Google

clspv open-source OpenCL kernel to Vulkan SPIR-V compiler - tracks top-of-tree LLVM and Clang - not a fork

clvk - prototype open-source OpenCL to Vulkan run-time API translator

Used by shipping apps and engines on Android e.g., Adobe Premiere Rush video editor - 200K lines of OpenCL C kernel code



clspv + Ancle

OpenCL over Vulkan
Samsung

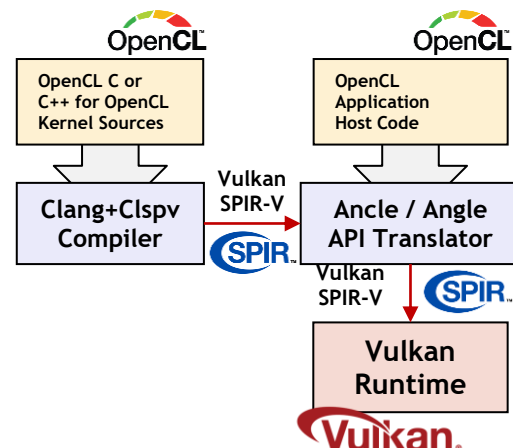
Integrates clspv and OpenCL runtime into Angle code base

Samsung Motivation

“OpenCL is widely used and deployed and is making a comeback thanks to ML”

“OpenCL is a favored high-level (front-end) compute language! Easier to write than Vulkan”

Ancle makes OpenCL a first-class citizen in Android by relying on Vulkan as its Native Driver”



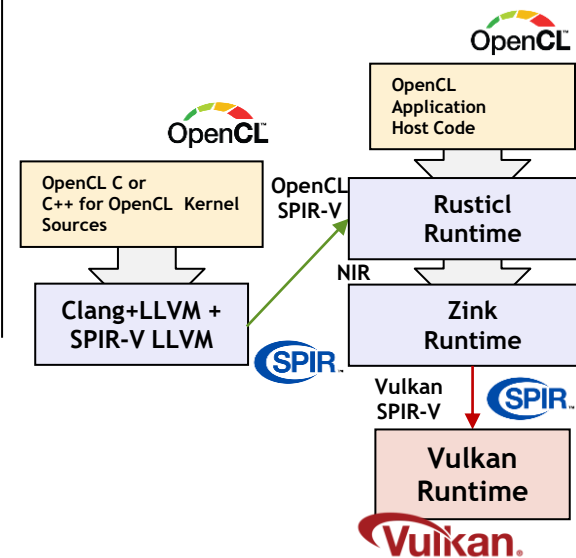
Rusticl over Zink

OpenCL over Vulkan
Mesa

The Zink Gallium driver emits Vulkan API calls and now supports OpenCL Kernels



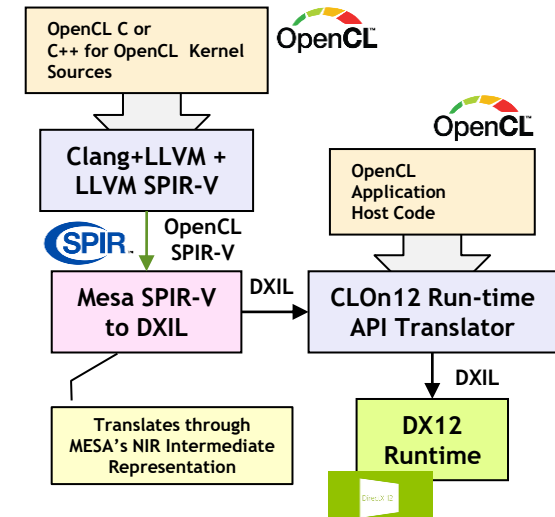
MESA



OpenCLOn12

OpenCL over DX12
Microsoft

GPU-accelerated OpenCL on any DX12 PC and Cloud instance (x86 or Arm)





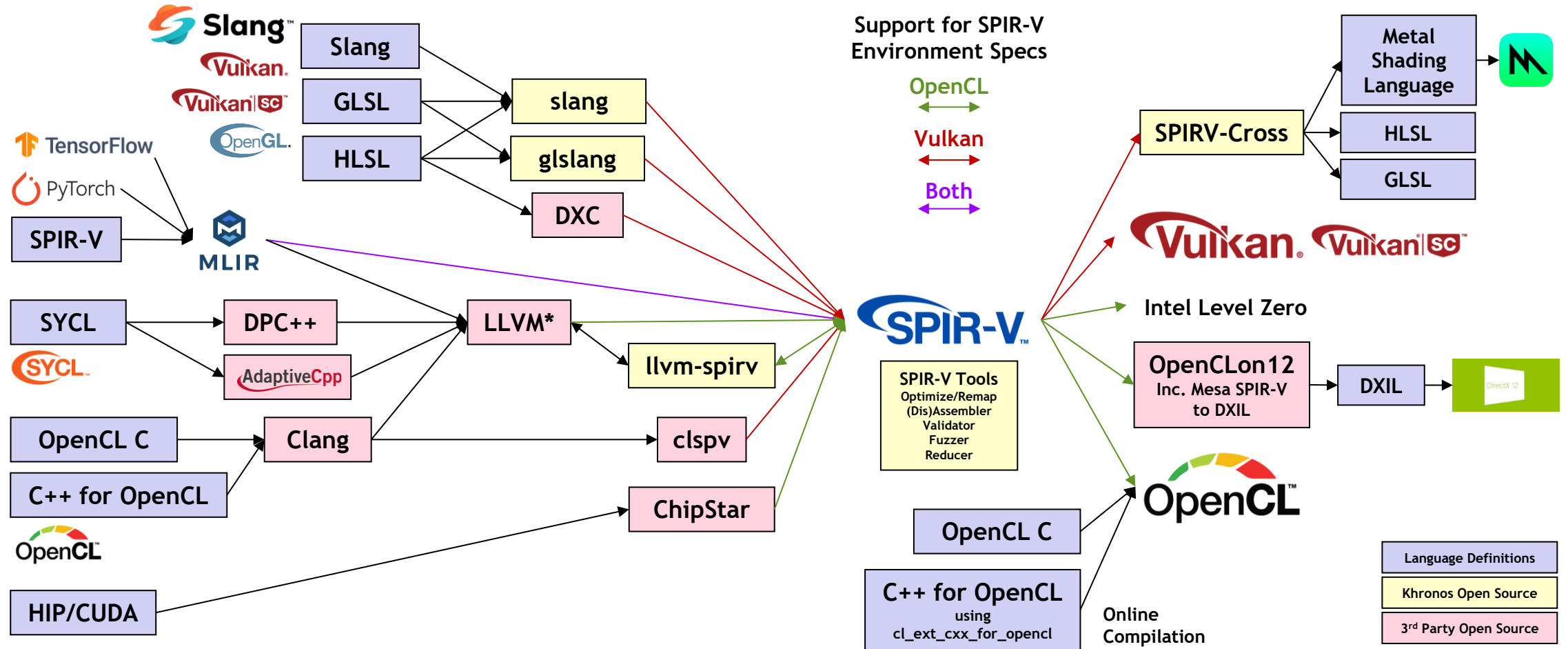
OpenCL 3.1 Feature Deep Dive

Key OpenCL 3.1 Feature:

- **OpenCL 3.1 requires support for SPIR-V consumption!**
 - Significantly enhances the viability of OpenCL as a language target
 - One of the most requested OpenCL features
 - Builds upon open source compiler and testing investments
- **OpenCL 3.1 also requires support for SPIR-V queries!**
 - Directly query the SPIR-V features supported by a device
 - Simplifies support for new SPIR-V extensions and capabilities



SPIR-V Ecosystem



MLIR is part of the LLVM compiler infrastructure

Key OpenCL 3.1 Feature:

- **OpenCL 3.1 requires support for sub-groups!**
 - Commonly used feature in highly tuned kernels, including AI kernels
 - Supported operations include shuffles, rotations, reductions, and more
 - Important building block for additional extensions in the future

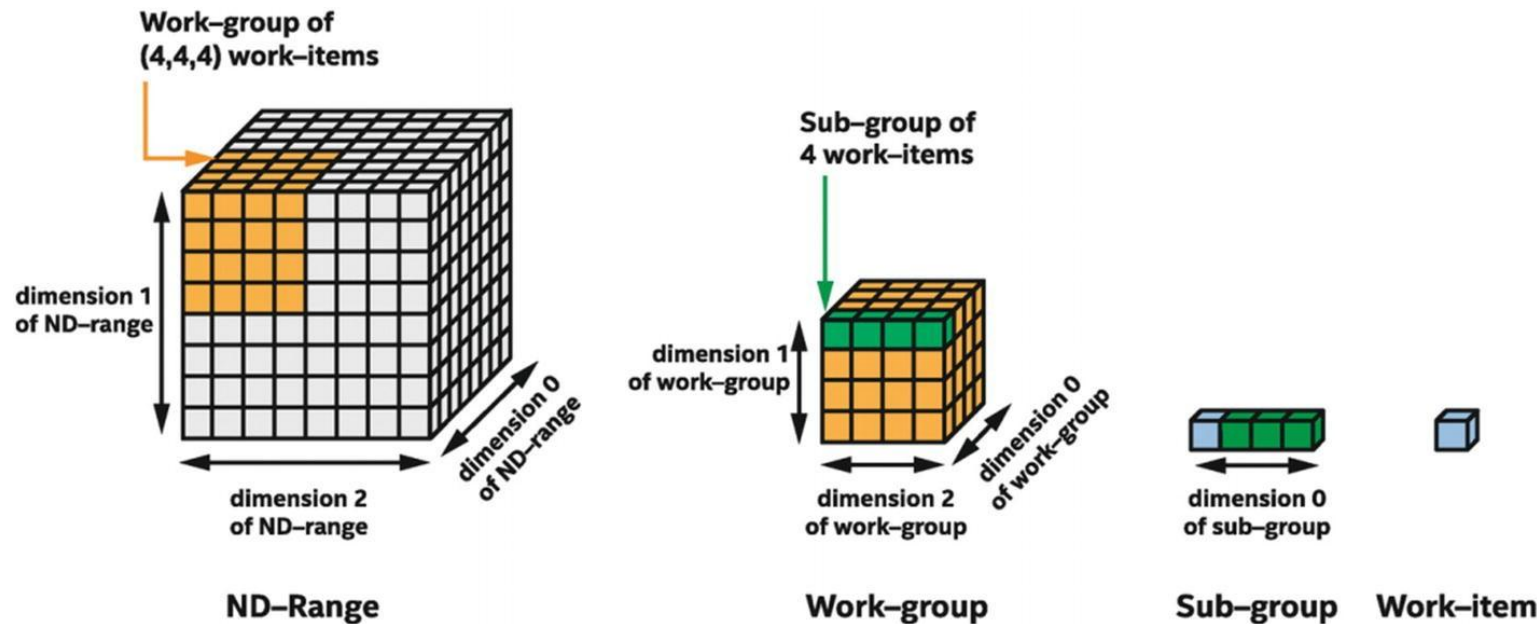
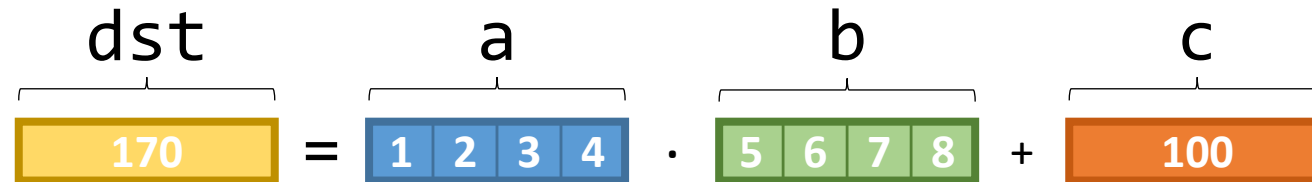


Diagram source: https://link.springer.com/chapter/10.1007/978-1-4842-9691-2_4/figures/12

More OpenCL 3.1 Features:

- **Integer Dot Products**

- Dot product of small integer vectors, with saturation and accumulation
- Building block for larger matrix multiplications

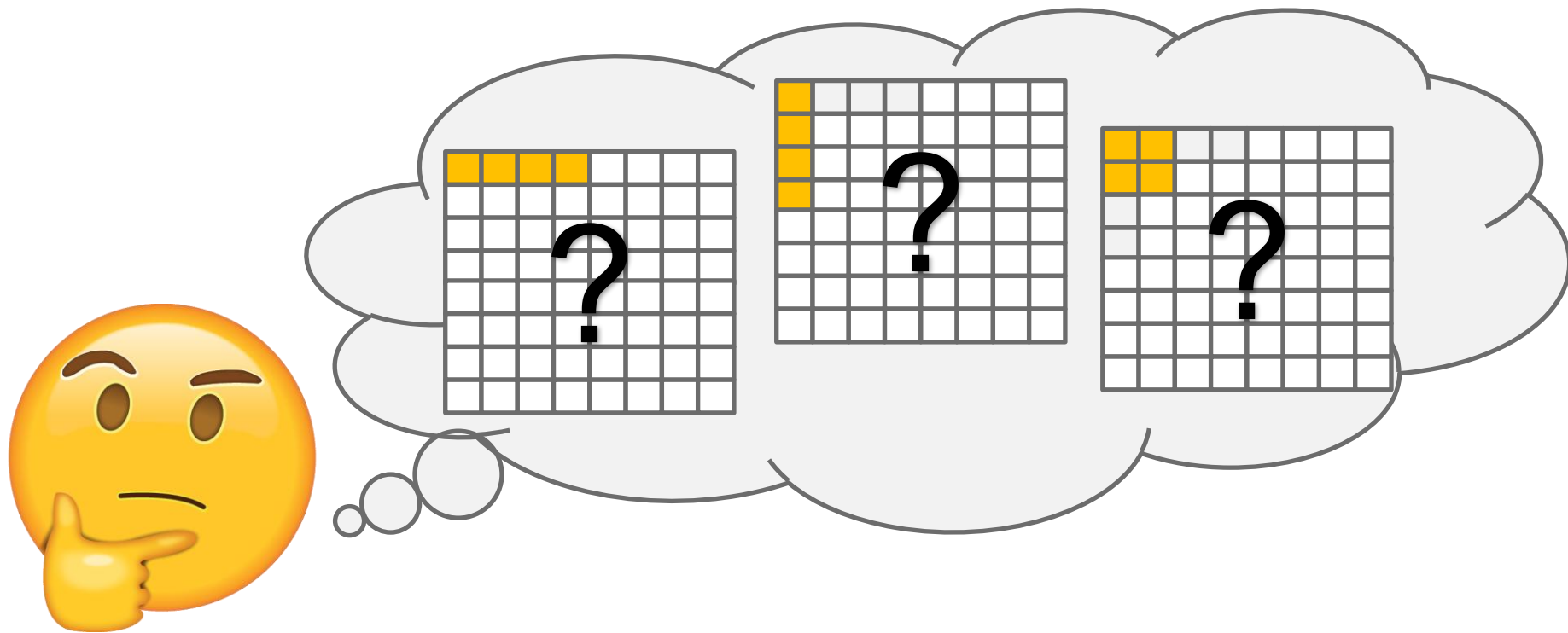


- **Extended Bit Operations**

- Insert, Extract, and Reverse bits in a bitfield
- Implemented with dedicated instructions on many devices

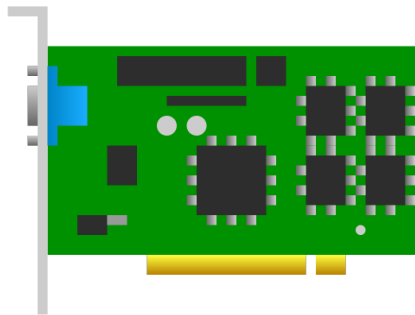
More OpenCL 3.1 Features:

- **Suggested Local Work-Group Size**
 - Reduces overhead by amortizing determination over multiple enqueues
 - Provides insight to profilers when the local work-group size is NULL



More OpenCL 3.1 Features:

- **Device UUID**
 - Uniquely identify devices in the system, including across APIs
 - Important for multi-device systems and external memory sharing



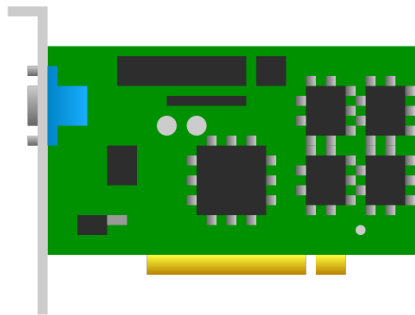
CL_DEVICE_UUID_KHR

86800BE2-0000-0000-7500-000000000000



VkPhysicalDeviceIDProperties::
deviceUUID

86800BE2-0000-0000-7500-000000000000



CL_DEVICE_UUID_KHR

86800BE2-0000-0000-7500-000000000000**1**

Even More OpenCL 3.1 Features:

- **OpenCL C 3.1 Kernel Language**
 - Use new language features without relying on extensions
- **OpenCL C printf**
 - Adds support for z (`size_t`) and t (`ptrdiff_t`) length modifiers
- **CL_DEVICE_HOST_UNIFIED_MEMORY**
 - Clarified meaning; can be used to query integrated vs. discrete GPUs
- **Local Memory Kernel Arguments**
 - Can be set to zero to indicate no local memory is needed

Even More OpenCL 3.1 Changes:

- **Execution Model Improvements**
 - Observing that an event is CL_COMPLETE is now a sync point
 - Matches intuition, previously needed to explicitly wait on the event
- **Memory Model Improvements**
 - Relaxed definition of “inclusive scopes” - scopes do not need to match
 - Also matches intuition, previous definition was unnecessarily strict

OpenCL 3.1 improves ergonomics, smooths out many rough edges!

Observations about OpenCL 3.1 Features:

There is very little that is truly new in OpenCL 3.1.

We think this is a good thing, not a bad thing!

We want core OpenCL APIs to be widely supported and used!

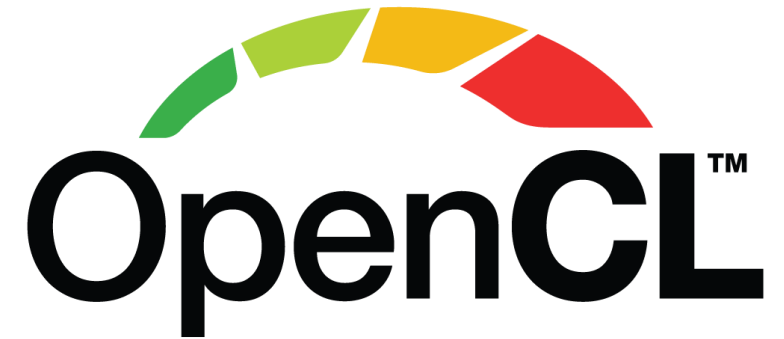
Innovate via Extensions, Standardize Best Practice

What's next?

Where do we go after OpenCL 3.1?

Lots of different options!

We would love your feedback!



Continue Innovation via Extensions

Finishing and finalizing experimental extensions:

- **Command Buffers**
 - Record groups of commands to lower overhead, increase performance
- **Unified SVM (Unified Shared Memory / USM)**
 - Represent allocations as pointers, not as handles (c1_mems)
 - More control over memory placement and migration
- **Cooperative Matrices**
 - Access dedicated matrix hardware for AI applications
- **Improved External Memory Sharing**
 - More handle types for better interoperability

Continue Innovation via OpenCL Extensions

Extensions in development:

- **New AI Data Types**
 - bfloat16, fp8, microscaling formats, ...
- **More Sub-group Controls**
 - Required sub-group size, required sub-group lane mappings
- **External Memory Improvements**
 - Improved tiling controls, DRM format modifiers, ...
- **And more!**

Today's OpenCL extensions are candidates for tomorrow's OpenCL core APIs!

OpenCL as a Substrate API

- Continuing to see usage of OpenCL indirectly!
 - As a heterogeneous Hardware Abstraction Layer (HAL)

Today:



chipStar

Vendor Inference
Engines

Possible Future?

OpenVXTM ?

OpenMP[®] ?

others?

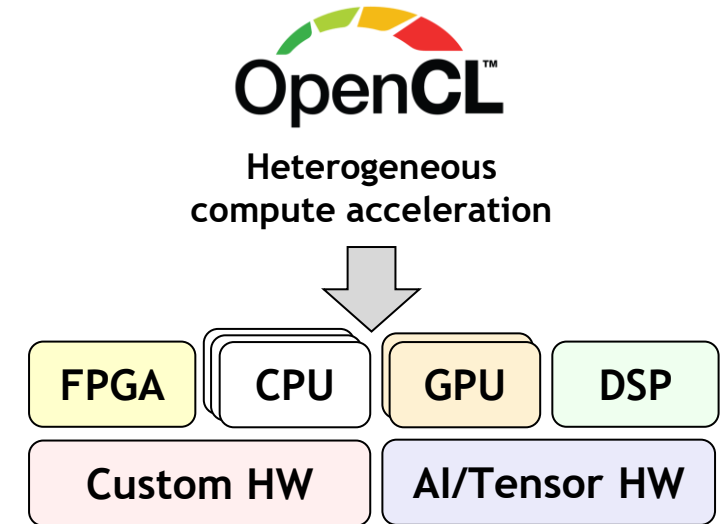
OpenCL for Safety Critical Applications?

- OpenCL is increasingly desired in Safety Critical markets
- Some member companies are exploring OpenCL SC
- Possible substrate API for SYCL SC?



OpenCL for More Devices?

- **Key OpenCL Strength: Device Diversity**
 - Can we expand OpenCL support to even more devices?
 - Via custom devices? More profiles?
- **Expanding interest in OpenCL from RISC-V**
- **Expanding interest in OpenCL in China**



Summary and Wrap-Up



OpenCL State of the Union



- **OpenCL 3.1 raises the bar!**
 - Adds features that are widely supported with proven value: SPIR-V consumption, sub-groups, much more...
 - A significant step forward for the heterogeneous compute ecosystem!
- **Extension pipeline remains active:**
 - Continuing to innovate via extensions
 - Driven by mobile, embedded, desktop, HPC, and AI markets
- **OpenCL is a key part of a layered ecosystem:**
 - OpenCL is a popular substrate layer for higher-level models - especially SYCL
 - OpenCL is a popular usability layer over lower-level models - especially Vulkan

Developer and Users:

Please Provide Your Feedback!

- **Give us your feedback on the OpenCL spec GitHub**
 - What could be added to the ecosystem to make you more productive?
 - What API and Language features do you most need?
 - <https://github.com/KhronosGroup/OpenCL-Docs>
- **Engage via the Khronos Discord Server**
 - <https://www.khr.io/khrdiscord>
- **Please download and run the GPUinfo OpenCL Hardware Capability Viewer**
 - <https://opencl.gpuinfo.org/download.php>

